

# Dual 16-bit DAC

# TDA1541

## PINNING

1	LE/WS*	latch enable input word select input
2	BCK*	bit clock input
3	DATA L/DATA*	data left channel input data input (selected format)
4	DATA R/SYS*	data right channel input system clock input
5	GND (A)	analogue ground
6	AOR	right channel output
7	DECOU	decoupling
8	DECOU	
9	DECOU	
10	DECOU	
11	DECOU	
12	DECOU	
13	DECOU	
14	GND (D)	digital ground
15	V <sub>DD2</sub>	-15 V supply voltage
16	n.c.	not connected
17	n.c.	
18	DECOU	decoupling
19	DECOU	
20	DECOU	
21	DECOU	
22	DECOU	
23	DECOU	
24	DECOU	
25	AOL	left channel output
26	V <sub>DD1</sub>	-5 V supply voltage
27	OB/TWC*	mode selection input
28	V <sub>DD</sub>	+5 V supply voltage

\* See Table 1 data selection input.

