

Project Explorer

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- msp430g2412.c
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- msp430g2413.c
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- msp430g2432.c
- msp430g2432.h
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- msp430p315.h
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829 /* These two bits are defined in the Special Function Registers */
830 /* #define WDTIE           0x01 */
831 /* #define WDTIFG         0x01 */
832
833 /*****
834 * Calibration Data in Info Mem
835 *****/
836
837 /* ADC10 Calibration Data - added by I. Cserny */
838 #define CAL_ADC_25T85      (0x0007u) /* Index for 2.5V/85Deg Cal. Value */
839 #define CAL_ADC_25T30      (0x0006u) /* Index for 2.5V/30Deg Cal. Value */
840 #define CAL_ADC_25VREF_FACTOR (0x0005u) /* Index for 2.5V Ref. Factor */
841 #define CAL_ADC_15T85      (0x0004u) /* Index for 1.5V/85Deg Cal. Value */
842 #define CAL_ADC_15T30      (0x0003u) /* Index for 1.5V/30Deg Cal. Value */
843 #define CAL_ADC_15VREF_FACTOR (0x0002u) /* Index for ADC 1.5V Ref. Factor */
844 #define CAL_ADC_OFFSET      (0x0001u) /* Index for ADC Offset */
845 #define CAL_ADC_GAIN_FACTOR (0x0000u) /* Index for ADC Gain Factor */
846
847 #ifndef __DisableCalData
848
849 SFR_8BIT(CALDCO_16MHZ); /* DCOCTL Calibration Data for 16MHz */
850 SFR_8BIT(CALBC1_16MHZ); /* BCSCTL1 Calibration Data for 16MHz */
851 SFR_8BIT(CALDCO_12MHZ); /* DCOCTL Calibration Data for 12MHz */
852 SFR_8BIT(CALBC1_12MHZ); /* BCSCTL1 Calibration Data for 12MHz */
853 SFR_8BIT(CALDCO_8MHZ); /* DCOCTL Calibration Data for 8MHz */
854 SFR_8BIT(CALBC1_8MHZ); /* BCSCTL1 Calibration Data for 8MHz */
855 SFR_8BIT(CALDCO_1MHZ); /* DCOCTL Calibration Data for 1MHz */
856 SFR_8BIT(CALBC1_1MHZ); /* BCSCTL1 Calibration Data for 1MHz */
857
858 #endif /* #ifndef __DisableCalData */
859
860 /*****
861 * Interrupt Vectors (offset from 0xFFE0)
862 *****/
863

```