

IRS2453(1)D(S) SELF-OSCILLATING FULL BRIDGE DRIVER IC

Features

- Integrated 600 V full-bridge gate driver
- CT, RT programmable oscillator
- 15.6V Zener clamp on V_{CC}
- Micropower startup
- Logic level latched shutdown pin
- Non-latched shutdown on CT pin (1/6th V_{CC})
- Internal bootstrap FETs
- Excellent latch immunity on all inputs & outputs
- ESD protection on all pins
- 14-lead SOIC or PDIP package
- 0.5 or 1.0 μs (typ.) internal deadtime
- RoHS compliant

Product Summary

| | |
|---|---|
| Topology | Full-bridge |
| V _{OFFSET} | 600 V |
| I _{o+} & I _{o-} (typical) | 180 mA & 260 mA |
| Deadtime (typical) | 1.0 μs (IRS2453D) 0.5 μs (IRS24531D) |

Package Options



14 Lead PDIP
IRS2453DPbF



14 Lead SOIC
(Narrow Body)
IRS2453(1)DSPbF

Typical Connection Diagram

