

SECTION 3 MECHANICAL ADJUSTMENTS

Precaution

- Clean the following parts with a denatured-alcohol-moistened swab:

record/playback head	pinch roller
erase head	rubber belts
capstan	idlers
- Demagnetize the record/playback head with a head demagnetizer. (Do not bring the head magnetizer close to the erase head.)
- Do not use a magnetized screwdriver for the adjustments.
- The adjustments should be performed with the rated power supply voltage (9 V) unless otherwise noted.

Torque Measurement

Mode	Torque meter	Meter reading
FWD	CQ-102C	2.95 – 6.86 mN • m (30 to 70 g • cm) (0.42 – 0.97 oz • inch)
FWD Back tension	CQ-102C	0.15 – 0.53 mN • m (1.5 to 5.5 g • cm) (0.021 – 0.076 oz • inch)
FF	CQ-201B	more than 5.88 mN • m (more than 60 g • cm) (more than 0.83 oz • inch)
REW	CQ-201B	more than 5.88 mN • m (more than 60 g • cm) (more than 0.83 oz • inch)

Tape Tension Measurement

Mode	Tension meter	Meter reading
FWD	CQ-403A	more than 100 g (more than 3.53 oz)

SECTION 4 ELECTRICAL ADJUSTMENTS

TAPE SECTION 0 dB = 0.775V

• Standard Output Level

Output terminal	HP OUT
load impedance	32 Ω
output signal level	0.25 V (–10 dB)

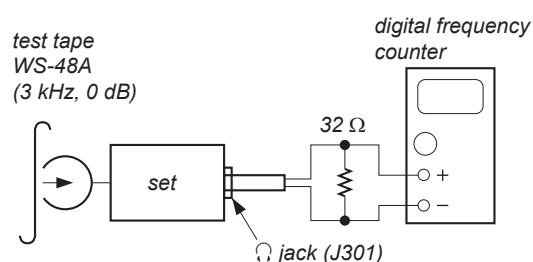
• Test Tape

Tape	Signal	Used for
WS-48A	3 kHz, 0 dB	tape speed adjustment
P-4-A063	6.3 kHz, –10 dB	azimuth adjustment

Tape Speed Adjustment

Procedure:

Mode: playback



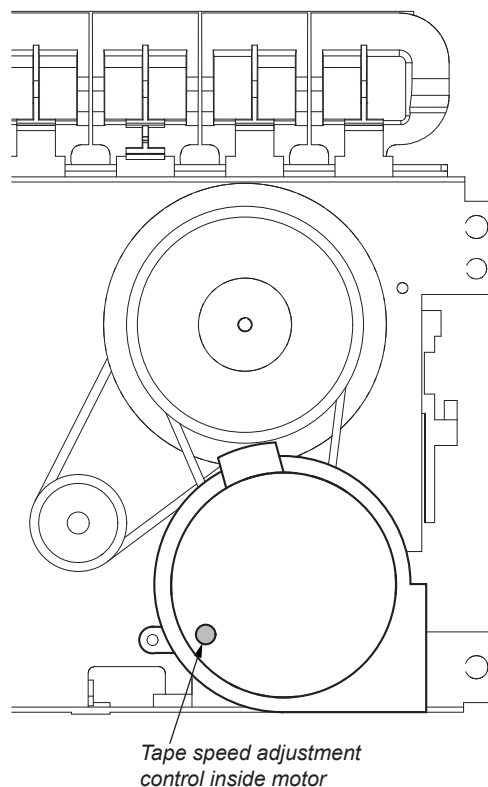
Adjust so that the value on the digital frequency counter is 3,000 Hz.

Specification Value:

Digital frequency counter
2,910 to 3,090 Hz

Adjust so that the frequency at the beginning and that at the end of tape winding are between 2,910 to 3,090 Hz.

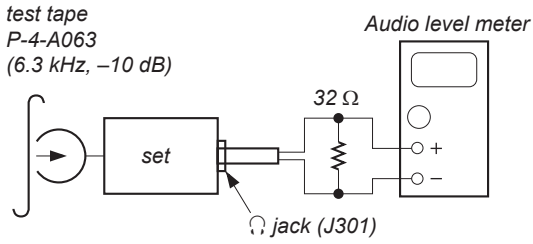
Adjustment Location:



Record/Playback Head Azimuth Adjustment

Procedure:

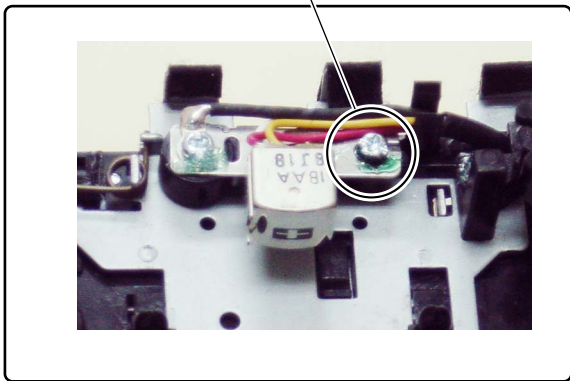
Mode: playback



Play back the test tape (P-4-A063) and adjust a screw for Azimuth adjustment in the head right side so that the output becomes maximum. The headphone output level is a range of -7 dB from -15 dB. After the adjustment, apply suitable Locking compound to the part adjusted.

Adjustment Location:

Record/playback
Azimuth adjustment screw

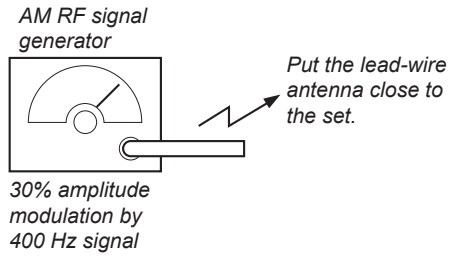


TUNER SECTION 0 dB = 1 μV

• **AM Section**

Setting:

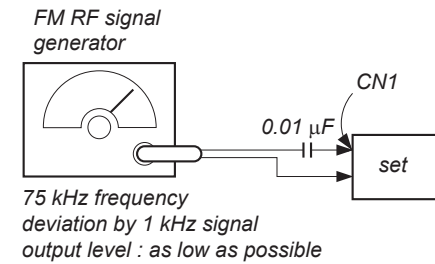
RADIO BAND•AUTO PRESET button: AM



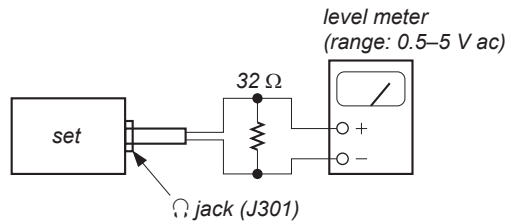
• **FM Section**

Setting:

RADIO BAND•AUTO PRESET button: FM



• **Connecting Level Meter (FM, AM)**



AM FREQUENCY COVERAGE CHECK		
Frequency Display	531 kHz	1,602 kHz
Adjustment Part	<confirmation>	

FM FREQUENCY COVERAGE CHECK		
Frequency Display	87.5 MHz	108 MHz
Adjustment Part	<confirmation>	

• **Abbreviation**

- AUS : Australian model
- KR : Korean model
- SP : Singapore model
- TH : Thai model
- TW : Taiwan model

CD SECTION

CD section adjustments are done automatically in this set.
In case of operation check, confirm that focus bias.

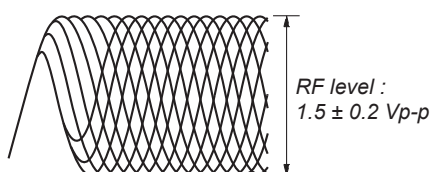
FOCUS BIAS CHECK

1. Connect the oscilloscope between IC801 pin ⑦⑨ and pin ⑧⑧ (or TP804 (RF) and TP711 (VREF)).
2. Insert the disc (PATD-012 (Tr 15)). (Part No. : 4-225-203-01)
3. Press the ► || (CD) button.
4. Confirm that the oscilloscope waveform is as shown in the figure below. (eye pattern)

A good eye pattern means that the diamond shape (◇) in the center of the waveform can be clearly distinguished.

- RF signal reference waveform (eye pattern)

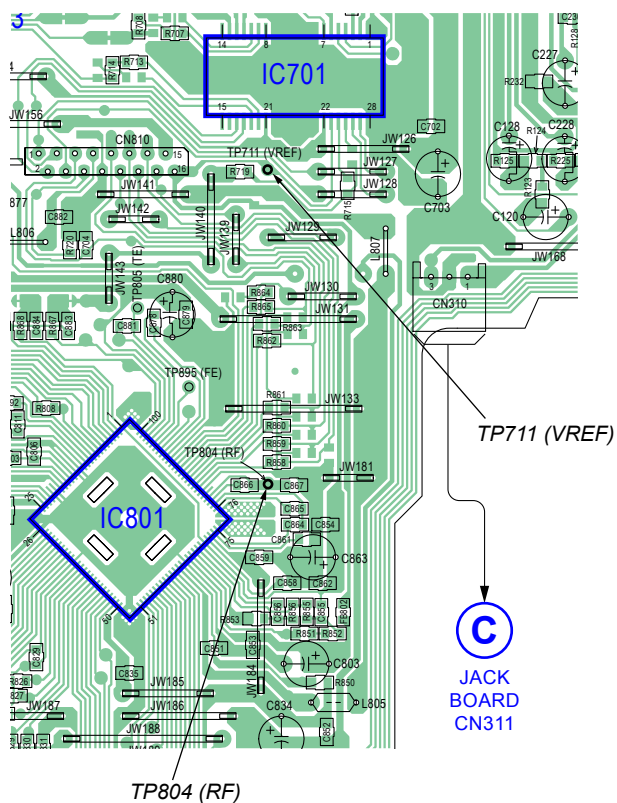
VOLT/DIV : 50 mV (10 : 1 probe in use)
TIME/DIV : 500 nS



When observing the eye pattern, set the oscilloscope for AC range and raise vertical sensitivity.

Test Point:

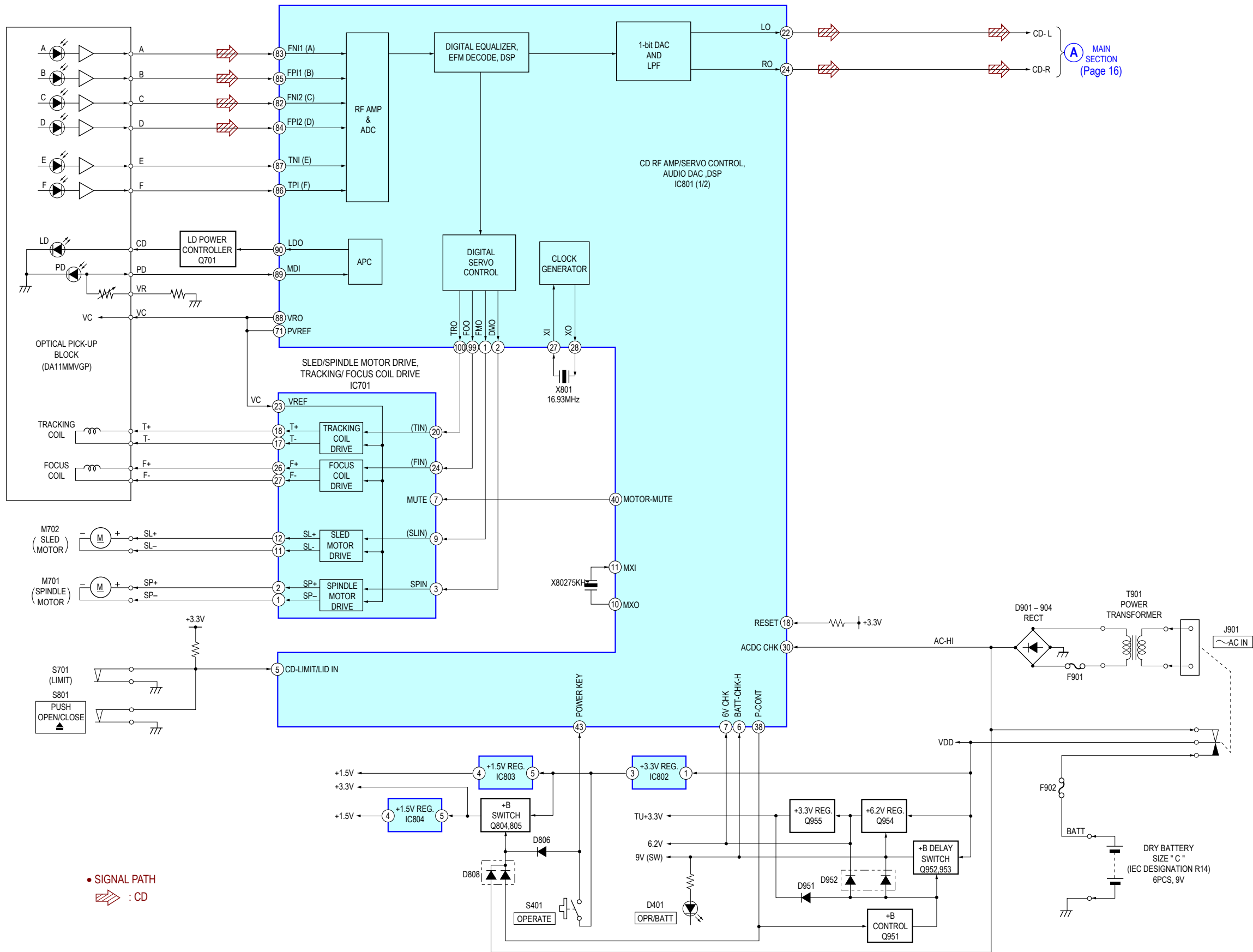
– MAIN BOARD (Conductor Side) –



MEMO

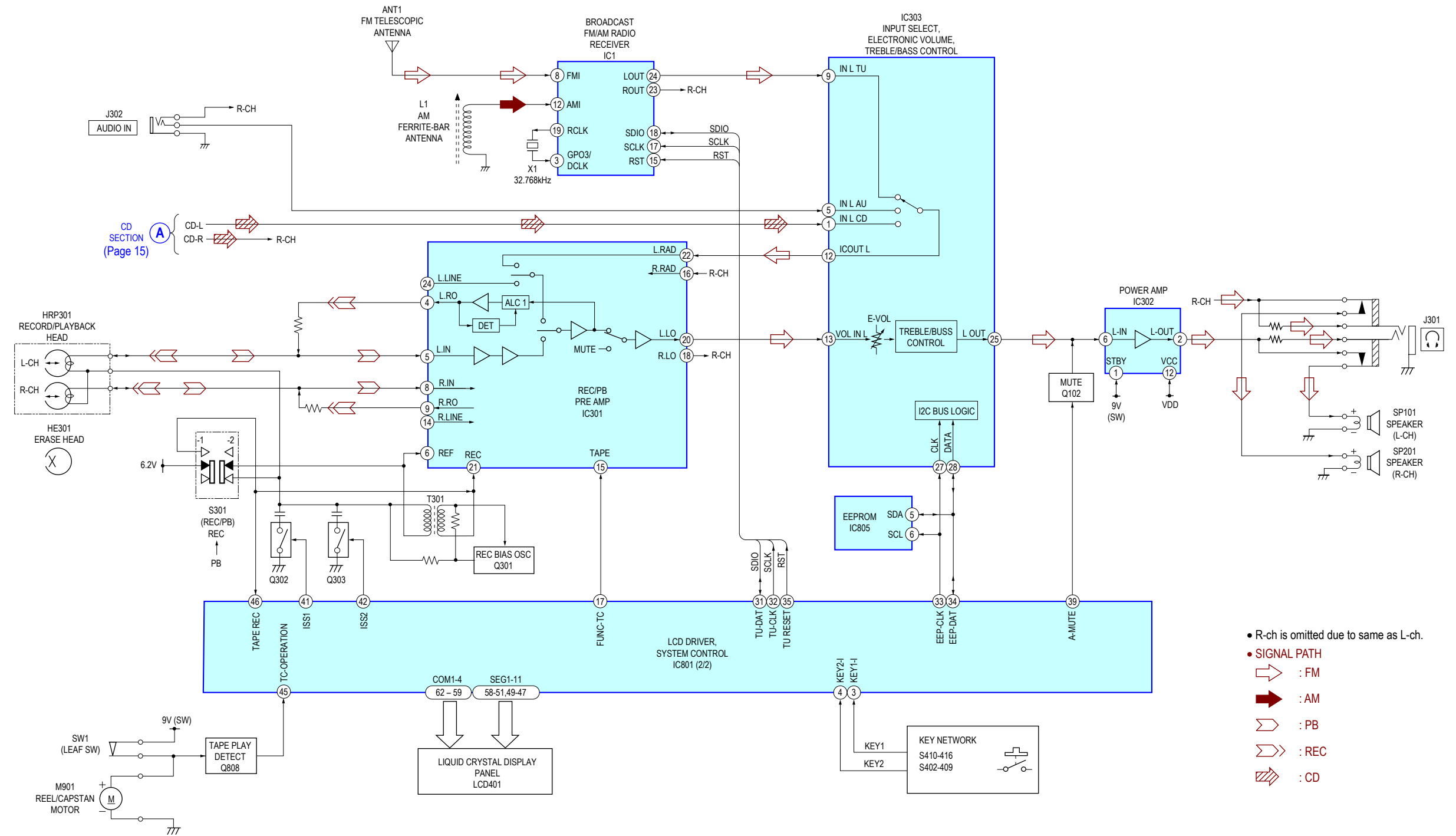
SECTION 5
DIAGRAMS

5-1. BLOCK DIAGRAM – CD Section –



A MAIN SECTION (Page 16)

5-2. BLOCK DIAGRAM – MAIN Section –



THIS NOTE IS COMMON FOR PRINTED WIRING BOARDS AND SCHEMATIC DIAGRAMS.
 (In addition to this, the necessary note is printed in each block.)

For Printed Wiring Boards.

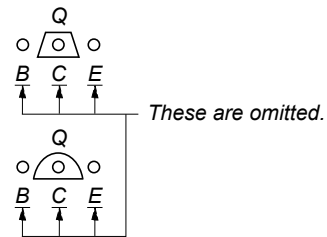
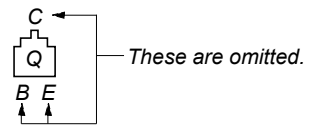
Note:

- : Parts extracted from the component side.
- : Parts extracted from the conductor side.
- : Indicates side identified with part number.
- △: Internal component.
- : Pattern from the side which enables seeing.
 (The other layers' patterns are not indicated.)

Caution:

Pattern face side: Parts on the pattern face side seen from the pattern face are indicated.
 Parts face side: Parts on the parts face side seen from the parts face are indicated.

• Indication of transistor.



• Abbreviation

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For Schematic Diagrams.

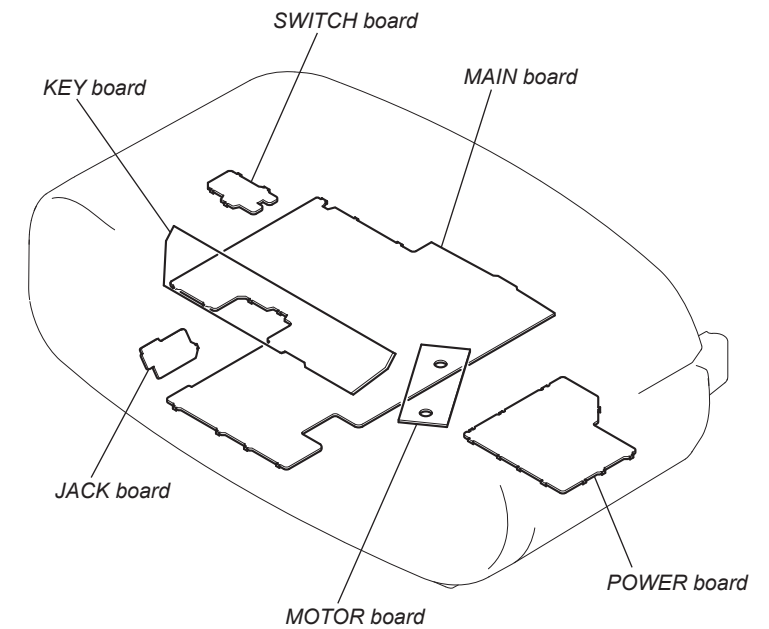
Note:

- All capacitors are in μF unless otherwise noted. (p: pF) 50 WV or less are not indicated except for electrolytics and tantalums.
- All resistors are in Ω and 1/4 W or less unless otherwise specified.
- △: Internal component.
- : Panel designation.

Note: The components identified by mark △ or dotted line with mark △ are critical for safety.
 Replace only with part number specified.

- : B+ Line.
- : Adjustment for repair.
- Voltagages and waveforms are dc with respect to ground under no-signal (detuned) conditions.
- no mark: FM
- (): AM
- < >: PB
- << >>: REC
- []: CD PLAY
- Voltagages are taken with VOM (Input impedance 10 M Ω). Voltage variations may be noted due to normal production tolerances.
- Waveforms are taken with a oscilloscope. Voltage variations may be noted due to normal production tolerances.
- Circled numbers refer to waveforms.
- Signal path.
- ⇨: FM
- ⇨: AM
- ⇨: PB
- ⇨: REC
- ⇨: CD
- Abbreviation
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• Circuit Boards Location

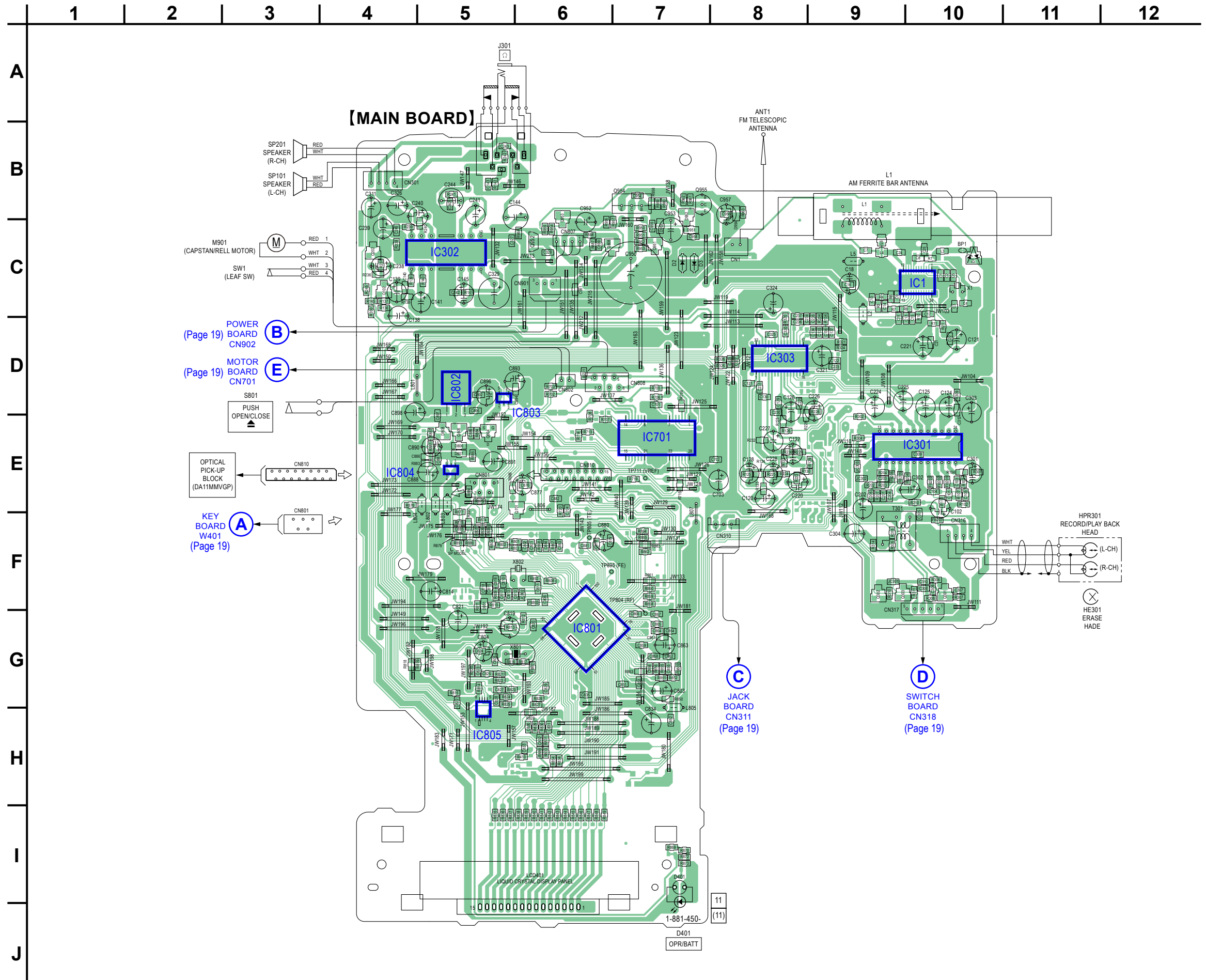


• Waveforms

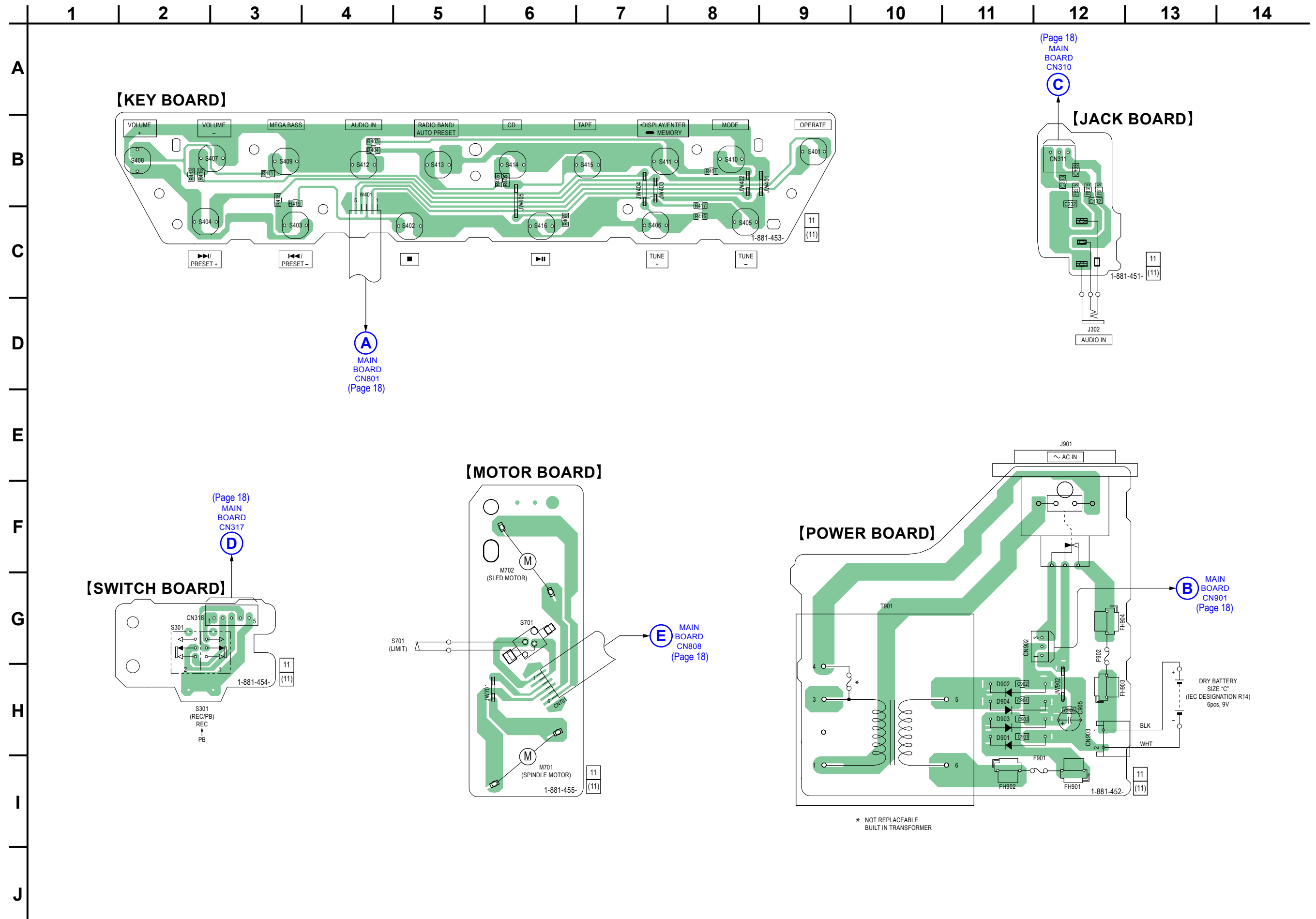
– MAIN Board –

<p>① IC1 (RCLK) – TUNER MODE –</p> <p>32.768 kHz</p> <p>0.7 Vp-p</p> <p>200 mV/DIV, 10 μs/DIV</p>	<p>④ IC801 (RFO) – CD PLAY –</p> <p>1.5±0.2 Vp-p</p> <p>500 mV/DIV, 0.5 μs/DIV</p>	<p>⑦ Q301 (REC)</p> <p>4.2 Vp-p</p> <p>18.1 μsec</p> <p>1 V/DIV, 10 μs/DIV</p>
<p>② IC801 (MXO)</p> <p>75 kHz</p> <p>2.7 Vp-p</p> <p>1 V/DIV, 5 μs/DIV</p>	<p>⑤ IC801 (FEI) – CD PLAY –</p> <p>1.5 V</p> <p>Approx. 100 mVp-p</p> <p>100 mV/DIV, 5 ms/DIV</p>	<p>⑧ HRP301 (YEL)</p> <p>40 Vp-p</p> <p>18.1 μsec</p> <p>10 V/DIV, 10 μs/DIV</p>
<p>③ IC801 (XO) – CD PLAY –</p> <p>16.93 MHz</p> <p>1.1 Vp-p</p> <p>1 V/DIV, 20 ns/DIV</p>	<p>⑥ IC801 (TEI) – CD PLAY –</p> <p>1.5V</p> <p>Approx. 170 mVp-p</p> <p>200 mV/DIV, 5 ms/DIV</p>	

5-3. PRINTED WIRING BOARD – MAIN Section – • See page 17 for Circuit Boards Location. •  : Uses unleaded solder.



5-4. PRINTED WIRING BOARDS – KEY, POWER Section – • See page 17 for Circuit Boards Location. •  : Uses unleaded solder.



5-5. SCHEMATIC DIAGRAM – MAIN Section (1/3) – • See page 17 for waveforms. • See page 25, 26 for IC Block Diagrams. • See page 28 for IC Pin Function Description of IC801.

