

What's New in Altium Designer 6.6

Summary

Article AR0140 (v1.0) November 07, 2006 Altium Designer 6.6 brings significant refinements to Variants combined with a number of smaller enhancements and improved system-wide support for existing technologies. Many of these improvements are based on feedback directly from you, the engineers and designers developing designs in Altium Designer.

Altium understands that improving the electronic product design platform isn't just about adding new features, but also about refining and strengthening existing ones. Electronics technology moves fast and your design systems need to keep up.

Altium Designer 6.6 delivers a number of enhancements designed to increase your effectiveness, improving features you depend upon to deliver better designs faster. Variants has been upgraded to support stronger support for printing and varying parameter values by updating from your libraries. In addition, a host of new commands and capabilities have further expanded the support of Variants system-wide.

With this release, support for a new pad shape Rounded Rectangle has been added. Recentlyintroduced 3D STEP has been expanded to include import from your preferred mechanical CAD system. Brand new example design and tutorial for Signal Integrity using one of Altium's own daughterboard designs show you how to determine optimum slew and drive settings for specific pins on an FPGA device.

These are just some of the new enhancements delivered by this significant new release of Altium Designer. To learn more about the new capabilities and productivity benefits offered in Altium Designer 6.6, read on!

Seeing is believing – read more and watch demos of Altium Designer 6.6

Altium's DEMOcenter gives you the opportunity to walk through the extensive design capabilities of Altium Designer featured as individual demos, each only taking a couple of minutes, making this a quick and easy way for you to browse the areas of most importance to you.

If you'd like to read more about updates in Altium Designer 6.6, as well as watch short videos about some of the exciting new features, then visit the **What's New in Altium Designer 6.6** page on the website and enjoy the action. Click the link below to read more and watch the videos.

http://www.altium.com/WhatsnewinAltiumDesigner/

Improved – Variants

Appreciating the importance of working with variants in the design process, Altium Designer 6.6 delivers new commands and capabilities to the *Assembly Variant Management* dialog as well as improvements system-wide:

- Ability to vary parameter values by updating from a library browse the libraries to select a component and all parameters are updated from the library component.
- Stronger support for printing Variants.



Figure 1. New options for handling schematic and PCB variant printing.

• Smart PDF and schematic output jobs support printing of physical documents that include variant information.

Smart PDF					
	Altium Des	igner Smart	PDF		
	Small PDF Structure Settings Select the structure to use for the PI	DF.			
33.07	Structure If checked, physical designators will physical sheets. A choice can also designators, net labels, ports, sheet I Use Physical Structure	be used in the exported PCB se made on which variant to u entries, sheet number and do	and schematic sheets will se and whether to display t ument number parameters	be expanded from logical sheets to he expanded physical names of	
	Variant Designators	Basic Version [None] Basic Version Deluxe Version	~		
	Net Labels Ports and Sheet Entries				
	Sheet Number Parameter Document Number Parameter				
			Can	icel <u>B</u> ack <u>N</u> ext	> <u>F</u> inish

Figure 2. Specifying use of an assembly variant as part of the Smart PDF setup options.

- Multiple variant fittings can be edited simultaneously.
- A whole variant can be copied and pasted.
- An Invert Selection command, as well as other new commands, are available.

Assembly Variant Management									
		Project Comp	onents		Low-Speed Version	High-Speed Version			
Logical	Comment 0.1uE	Designator	Document 4 Port LIABT and Line Drivers SchDoc		Fitted	Edit Selected			
011	74HC32	U11	ISA Bus and Address Decoding.SchDoc	•	Fitted	Set Selected As Fitted Set Selected As Not Fitted			
						Only Show Varied Components Add Variant Remove Variant Edt Variant Copy Variant Paste Variant			
	F	^o arameter Varia	ations C5		Low-Speed Version				
Parameter Na	ame	Original Value	•		New Value	Cut			
Comment		0.1uF		~	10uF	Dacte			
Manufactuer		BC Componer	nts	✓	Panasonic				
Manufactuer I	P/N	2222 370 221	04	v	ECA-1HHG100	Report			
Part Number		C001037		v	C001618	Save All			
Tolerance		0.05		~	0.2	Save Selected			
Voltage-Rated	ł	100V		~	50V	Select All			
						Select Column Invert Selection			
E Mer	Cross Probe	ncel							

Figure 3. Many new commands now available from the right-mouse menu in the Assembly Variants Management dialog

- Component and parameter variation grids can be configured to show only values that are different from the original.
- BOM file name format supports display of variant names in the following format: <Bill of Materials>
 Project Name> (<Variant Name>). You can also make a choice of which variants to use with your BOM.
- Parameter variation values can be generated in a detailed report.
- Variant information is preserved during re-annotation.

New – Rounded Rectangle pad shape

Altium Designer 6.6 expands support for pad shapes to include new *Rounded Rectangle*. Rounded Rectangle pad shapes are defined in the redesigned PCB *Pad* dialog, giving you immediate visual feedback on the design of the pad.

Support for Rounded Rectangle pad shapes include:

- User-defined Corner Radius allows you to specify the corner radius and allows a range of 0 100%.
- Updated PCB Pad dialog giving instant visual feedback on the pad design.

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	di.		
Top Layer (POWER	} {GROUND {Bottom Layer}Mu	ti-Layer /	
Location		Size and Shape	
x	103.505mm	💿 Simple 🔿 Top-Middle-Bott	om 🔘 Full Stack
Y	-3.302mm	V.Cita V.Cita	Corner Change Radius (%)
Rotation	0.000	3.81mm 3.81mm	Rounded Rectand V 50%
Hole Information			
Hole Size	0.762mm		
<u> Bound Bound Bound Constant Const Const Const Constant Constant </u>		E	Edit Full Pad Layer Definition
🚫 Square		Desta Mask Excession	
Slot		Fundamentation value from rules	
		Cxpansion value nom rules	
Description			Omen
Properties	7	Specify expansion value	Omm
Properties Designator	7 Multi Jung	Specify expansion value	Omm
Properties Designator Layer	7 Multi-Layer	Specify expansion value Solder Mask Expansions Expansion value from rules	Dmm
Properties Designator Layer Net	7 Multi-Layer V No Net V	Specify expansion value Solder Mask Expansions Expansion value from rules Specify expansion value	0mm
Properties Designator Layer Net Electrical Type	7 Multi-Layer v No Net v Load v	Specify expansion value Solder Mask Expansion Expansion value from rules Specify expansion value Force complete tenting	0mm 0.102mm on top
Properties Designator Layer Net Electrical Type Testpoint Plated	7 Multi-Layer No Net Load Top Bottom	Solder Mask Expansion value Solder Mask Expansion Expansion value from rules Specify expansion value Force complete tenting Express complete tenting	0.102mm on top
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Properties Designator Layer Net Electrical Type Testpoint Plated Locked	7 Multi-Layer No Net Load Top Bottom	Solder Mask Expansion value Solder Mask Expansions Expansion value from rules Specify expansion value Force complete tenting Force complete tenting	0.102mm on top on bottom

Figure 4. Create Rounded Rectangle pad shapes in your PCB.

• Updated PCB Pad Layer Editor dialog shows graphic representation of pad design.

Pad Layer Ed	litor						? 🗙
Attributes on Lay	/er			Layer Stack Ref	erence	Absolute Layer	
Shape	X Size	Y Size	Corner Radius (%)	Name	Index	Name	Index /
Rounded Rectar Round	ng 3.81mm 3.81mm	3.81mm	50%	Top Layer Bottom Layer	0 1	TopLayer BottomLayer	1 32
🔽 Only show la	ayers in laye	rstack				ОК	Cancel

Figure 5. Define your pad size and shape for each layer in the pad stack, on a layer-by-layer basis.

New – STEP (3D File Format) import

STEP, the **ST**andard for the **E**xchange of **P**roduct model data is becoming a preferred standard for ECAD to MCAD data exchange – allowing transfer of 3D models between CAD applications. Altium Designer 6.6 expands new STEP support to include importing a 3D STEP format file from your preferred mechanical CAD system.



Figure 6. Once imported and associated to components, 3D STEP format models allow accurate representations of PCB boards to be created and transferred to Mechanical CAD applications.

3D STEP models are imported into PCB3D Library files (*.PCB3DLib) files and then associated with the component symbol in the same way that other models are associated with component symbols.



Figure 7. Imported 3D STEP components as seen in Altium Designer 6.6.

Improved – IPC Footprint Wizard

Available through the **Tools** menu when a PCB library is the active document, the new IPC Footprint Wizard creates IPC-compliant component footprints. Rather than working from footprint dimensions, the **IPC Footprint Wizard** uses dimensional information from the component itself in accordance with the standards released by the IPC. For Altium Designer 6.6, it has been enhanced by the addition of a preview window and support for additional package types.

Some of the IPC Footprint Wizard enhancements include:

 A variety of new footprint generators are included, tailored to suit your board's density – Chip Components (Capacitor, Inductor and Resistor), QFN, SOJ, SOT23 (3-Leads, 5-Leads and 6-Leads), SOT143/343 and SOT223.



Figure 8. Quickly create IPC-compliant component footprints based on component dimensions in the new **IPC Footprint Wizard**.

- Overall packaging dimensions, pin information, heel spacing, solder fillets and tolerances can be entered and immediately viewed.
- Mechanical dimensions such as Courtyard, Assembly, and Component Body Information can be entered.
- Wizard is re-entrant and allows reviewing and making adjustments easy. Previews of the footprint are shown at every stage.
- The finish button can be pressed at any stage to generate the currently previewed footprint.

New – Signal Integrity example

Determining how hard you can drive signals before ringing and crosstalk start to affect performance is

directly related to finding optimum slew and drive settings for specific pins of an FPGA device. A new example has been added to the Signal Integrity examples folder (\Examples\Signal Integrity) that explores this. This example is based on one of Altium's own daughterboard designs, the NBP28, which features a Xilinx Spartan 3, a Sharp LH79520 incorporating an ARM 7 processor, SRAM and Flash RAM.

An accompanying tutorial - *TU0126 Checking Signal Integrity on an FPGA Design* - explores in detail how you can use Altium Designer's Signal Integrity Analyzer to determine optimum Slew and Drive settings for the data pins of the Spartan 3 device in this design.

Signal Integrity Pres	ferences				?×
General Configuration	Integration	Accuracy	DC Ana	ilysis	
Simulation					
Ignore Stubs (mil):	39.37]			
Total Time (s):	100.00n	Time St	ep (s):	100.0p	
Coupling					\leq
Max Dist. (mil):	787.4	Min Len	gth (mil):	3.937	\supset
				<u>D</u> efa	ults
		(OK	C.	ancel

Figure 9. Set criteria to identify coupled nets.

The tutorial includes:

- Setting up IBIS models for devices in your design.
- Running reflection analyses on data lines at different Slew and Drive settings.
- Identifying coupled nets and analyzing crosstalk.



Figure 10. Experiment with Slew and Drive settings through the FPGA Signal Manager to see the effect on crosstalk.

A link to this tutorial can be found in the **Documentation Library** » **Design Verification** » **Signal Integrity** area of the **Knowledge Center** panel.

Alternatively, the document can be found directly within the \Help folder of the installation.

Improved – Bill of Materials

Data from both the schematic and the PCB can now be included into a single Bill of Materials (BOM) report – source information is based on property information taken from the PCB in the event you need to customize and use the report generation for more than a BOM. An example would be for generation of a pick and place file where every placement machine wants the data (such as X, Y location) in a different column order and in different file formats.

With the required schematic or PCB documents open, select **Reports** » **Bill of Materials**. The *Bill of Materials for Project [project_name] (PCB_document)* dialog displays. In the parameter listing, the **Bill of not struggister and the project**.

Bill of Materials For Project [PCB_VideoMux.PrjPCB] (PCB Document : VideoMuxBoard.PcbDoc)									
Grouped Columns	s Show	Footprint 🗠 💌	Layer 💌	Center-X(Mil)	Center-Y(Mil)	Pad-X(Mil) 💌	Pad-Y(Mil)	Rotation 🔼	
Comment		CC2012-0805	Тор	1180, 1380, 1580, 1780,	300, 2060	1180, 1380, 1580, 1780,	180, 380, 580, 780, 1560, 1	0, 90	
Footprint	~	CC2012-0805	Тор	680, 960, 1060, 2460, 25	480, 2400, 2620, 3220,	680, 960, 1060, 2460, 25	-40, -320, 60, 1460, 1560, 1	0, 90, 270	
		CC2012-0805	Тор	3840, 4020	1000, 1100	3802.598, 3982.598	2802.598, 2982.598	0	
		CC2012-0805	Top	3840, 4020, 4200	1000, 1100	3802.598, 3982.598, 416	2802.598, 2982.598, 3162.	0	
		CC2012-0805	Тор	4300	1280	4262.598	3262.598	0	
		CC2012-0805	Тор	820, 2200	1480	820, 2200	-180, 1200	270	
		CC2012-0805	Тор	360, 380, 580, 680, 780,	1780, 1900, 2540, 2980	322.598, 342.598, 580, E	-120, -220, -320, -420, -657	0, 270	
	\wedge	CC2012-0805	Тор	360, 380	2620, 3080, 3540, 4000	322.598, 342.598	-657.402, -677.402	0	
All Columns	Show A	CC2012-0805	Тор	3760	3080	3722.598	2722.598	0	
🗱 Center-X(Mil)		CC2012-0805	Тор	4280	3280	4242.598	3242.598	0	
Center-X(mm)	H I	CC2012-0805	Тор	3160	4540	3160	2160	90	
B Center-Y (Mil)		PCB_SWITCH	Тор	3580	4460	3580	2580	0	
🕎 Center-Y(mm)		B3FS_SWITCH	Тор	260 107	2799.803, 3259.803, 3	417.874	-582.126	0	
🕮 Layer	✓	QFP14×14_64	Тор	1396.221, 2776.221	1014.961	1160, 2540	160, 1540	90	
🕮 Pad-X(Mil)	✓	QFP10×10_44	Isp	4062.519	1683.78	4220	3220	270	
💖 Pad-X(mm)		S0-J32	Тор	2100	2520	1725	725	0	
IIII) Pad-Y(Mil)	✓	V020	Тор	820	2660	929.252	-70.748	90	
时 Pad-Y(mm)		TG220-5V	Тор	4421.26	2866.142	4342.52	3342.52	270	
IIII Ref-X(Mil)		QFP28X28-G208/P.5N	Тор	2059.977	3799.976	2646.591	1646.591	180	
Bef-X(mm)		GUIC9	Тор	4021.89	3255	4140	3140	180	
Her-Y[Mil]		T0220-5v	Тор	4421.26	3846.142	4342.52	3342.52	270	
Belation		QC49/SMD	Тор	760, 2140	1080	760, 2140	-240, 1140	90 💌	
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Add to Project Depen Exported Relative Path to Template File									
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Figure 11. When configuring the Bill of Materials report using the Report Manager dialog, simply enable the **Include Parameters From PCB** option. This option will only be available if there is a PCB document in the project file.

Exporting your report

Parameters are a universal feature of Altium Designer and can be added to the project, a document, a component and other objects. Project and document parameters can be extracted from the design and included in the BOM report.

When exporting your data from the BOM, you can include two new PCB document fields in your Excel templates:

- Field=PCBDataSourceFullName displays the full name of the PCB data source.
- Field=PCBDataSourceFileName displays the file name of the PCB data source.

Improved – Drill Drawing Symbols table

Altium Designer 6.6 expands support for slotted holes in PCB pads with the addition of slot information added to the Drill Drawing Symbols table – providing more options for board fabrication and smoothing the process to manufacturing. Appropriate slot information is included at the time of output file generation.

Improvements for slotted holes in Drill Drawing include:

• Support for extended numbers of symbols improved to automatically switch to letters after the graphic symbols run out.

AA U510 0.3mm (II.811m1) PTH Pound AC 265 0.4mm (U5.748m1) PTH Round AB 85 0.5mm (U3.685m1) PTH Round AE 19 0.6mm (23.622m1) PTH Pound AE 19 0.6mm (23.622m1) PTH Pound AE 10 0.7mm (23.622m1) PTH Pound Z 12 0.8mm (31.486m1) PTH Pound Z 14 0.92mm (33.737m1) PTH Pound G 1063 1mm (53.37m1) PTH Pound N 3 1.9mm (51.181m1) PTH Pound N 2 2.9mm (90.51m1) PTH Pound K 5 1.9mm (51.181m1) PTH Pound <t< th=""><th>Symbol H</th><th>Hit Count</th><th>Tool</th><th>Size</th><th>Physical</th><th>Length</th><th></th><th>Path</th><th>Length</th><th>Plated</th><th>Hole</th><th>Туре</th></t<>	Symbol H	Hit Count	Tool	Size	Physical	Length		Path	Length	Plated	Hole	Туре
AC 265 0.4mm (15.748m1) FTH Pound AB B5 0.5mm (15.687m1) PTH Pound AB IB 0.5mm (15.687m1) PTH Pound AD 25 0.7mm (27.558m1) PTH Pound AD 25 0.7mm (27.558m1) PTH Pound AF 1 0.7112mm (28m1) PTH Pound C1 0.7mm (27.558m1) PTH Pound C1 0.9mm (31.496m1) PTH Pound C1 0.9mm (35.439m1) PTH Pound C1 14 0.92mm (35.307m1) PTH Pound C3 1.5mm (15.307m1) PTH Pound M 3 1.5mm (15.407m1) PTH Pound M 3 1.5mm (51.181m1) PTH Pound D1 1.6mm (62.982m1) PTH Pound D1 2 2.3mm (18.6414m1) PTH Pound D1 2 2.3mm (18.6414m1) PTH Pound D1 2 2.3mm (18.64148m1) PTH	AA L	.510	0.3mp	(11.811mil)						ртн	Round	
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H I O.Bmn Gl. 496mLi 2mm 72,74mLi I.2mm (47,244mLi) PTH Stat D 4 Inmut (33,37mLi) 3mm (118,11mLi) 2mm 74mLi) PTH Stat E 2 Inmut (33,37mLi) 3,5mn (13,7787mLi) 2,5mn 97.4mLi) PTH Stat B 2 2,388mm (94,016mLi)(8,9mm (744,095mLi)) 16,512mm (650,073mLi) NPTH Stat A 10 2,388mm (94,016mLi)(8,9mm (744,095mLi)) 16,512mm (650,073mLi) NPTH Stat 3326 Total					1.5mm (59.0							
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3326 Total		.0	2.388m	w (94.016m.1	54.1nm (212	29.921mil	51.712m		35.906nil)	NPTH		
Slot definitions : Rout Parh Length = Calculated from tool start centre position to tool end centre	3	3326 Total										
	SLOT det1	INITIONS : R	out Pa	th Length =	Calculated	trom too.	START	Centr	e position	TD TOOL	end c	entre .

• Letter symbols now automatically allow an extended sequence (A...Z, AA, AB, etc.).

Figure 12. Enhancement for improved readability as well as slot information can now be viewed in the Drill Drawing.

 Reworked for greater overall presentation, the Drill Drawing Symbols table features the addition of headers and column separators. Symbols are drawn in the table at the same height as the rest of the table text for improved legibility. This allows for a clearer drill drawing utilizing small symbols.

Revision History

Date	Version No.	Revision
07-Nov-2006	1.0	Altium Designer 6.6 release

Software, hardware, documentation and related materials:

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