# AN6557, AN6558, AN6558S

## Dual Low Noise, High Slew Rate Operational Amplifiers

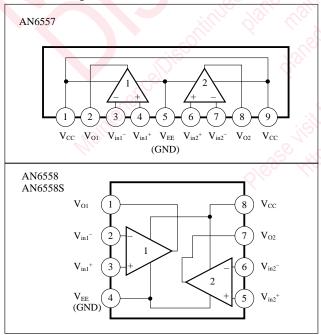
#### Overview

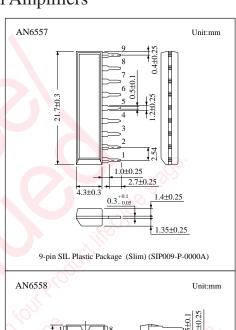
The AN6557, the AN6558, and the AN6558S are low noise, high slew rate dual operational amplifiers with phase compensation circuits built-in. They are suitable for application to various electronic circuits such as active filters and audio preamplifiers. Moreover, they are high output current type and can also be used as headphone amplifiers.

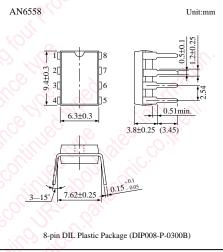
#### ■ Features

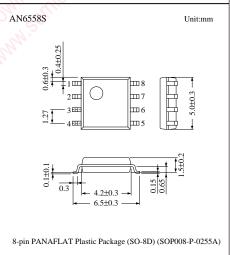
- Phase compensation circuit
- High voltage gain:Gv=100dB typ.
- Low noise: Vni=0.9µ Vrms typ.
- High slew rate:SR=6V/μ s typ.
- High output current:Io=25mA typ.

#### ■ Block Diagram









### ■ Pin Descriptions

(AN6557)

Pin No.	Pin name				
1	V <sub>CC</sub>				
2	Ch. 1 output				
3	Ch. 1 inverting input				
4	Ch. 1 non inverting input				
5	V <sub>EE</sub> (GND)				
6	Ch. 2 non inverting input				
7	Ch. 2 inverting input				
8	Ch. 2 output				
9	V <sub>CC</sub>				

#### (AN6558, AN6558S)

Pin No.	Pin name
1	Ch. 1 output
2	Ch. 1 inverting input
3	Ch. 1 non inverting input
4	V <sub>EE</sub> (GND)
5	Ch. 2 non inverting input
6	Ch. 2 inverting input
7	Ch. 2 output
8	V <sub>CC</sub>

### ■ Absolute Maximum Ratings (Ta=25°C)

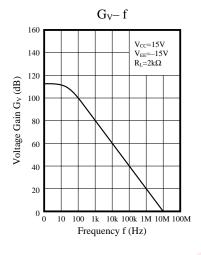
Parameter		Symbol	Rating	Unit	
Voltage	Supply voltage	$V_{CC}$	±18	V	
	Differential input voltage	$V_{ m ID}$	±30	V	
	Common-mode input voltage	V <sub>ICM</sub>	±15	V	
Power dissipation	AN6557, AN6558	- P <sub>D</sub>	500	W	
	AN6558S		360	mW	
Operating ambient temperature		$T_{ m opr}$	-20 to +75	°C	
Storage temperature	AN6557, AN6558	T <sub>stg</sub>	-55 to +150	· C	
	AN6558S		-55 to +125		

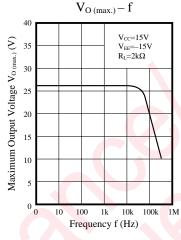
### ■ Electrical Characteristics (V<sub>CC</sub>=15V, V<sub>EE</sub>=-15V, Ta=25°C)

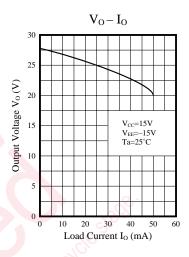
Parameter	Symbol	Condition	min	typ	max	Unit
Input offset voltage	V <sub>I (offset)</sub>	$R_{S} \leq 10 k\Omega$		0.3	3	mV
Input offset current	$I_{IO}$	The All is Co. The	10	10	200	nA
Input bias current	I <sub>Bias</sub>	e li gos dili	2	1300	2000	nA
Voltage gain	Gv	$R_L \ge 2k\Omega$ , $V_0 = \pm 10V$	86	100		dB
Maximum output voltage	V <sub>O (max.)</sub>	$R_L \ge 10 k\Omega$	±12	±14		V
Waxiiiuiii output voitage		I <sub>O</sub> =25mA	±10	±12		V
Common-mode input voltage width	$V_{CM}$	10, 501,	±12	±14		V
Common-mode rejection ratio	CMR	140. W;	70	100		dB
Supply voltage rejection ratio	SVR	is un		10	150	μV/V
Power consumption	P <sub>C</sub>	R <sub>L</sub> =∞		150	240	mW
Slew rate	SR	$R_L \ge 2k\Omega$		6		V/µs
Equivalent input noise voltage	V <sub>ni</sub>	$R_S=1k\Omega$ , DIN/AUDIO		0.9		μVrms

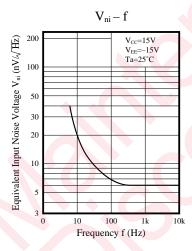
## **Panasonic**

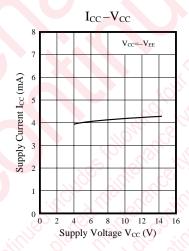
#### ■ Characteristics Curve

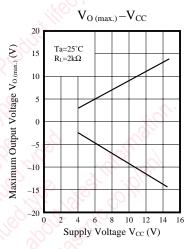




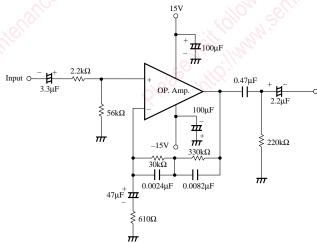








# ■ Application Circuit RIAA Amplifier



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