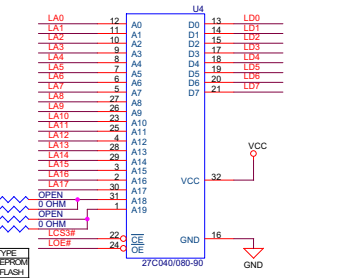
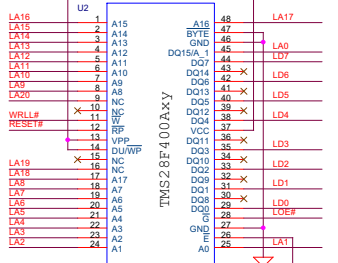


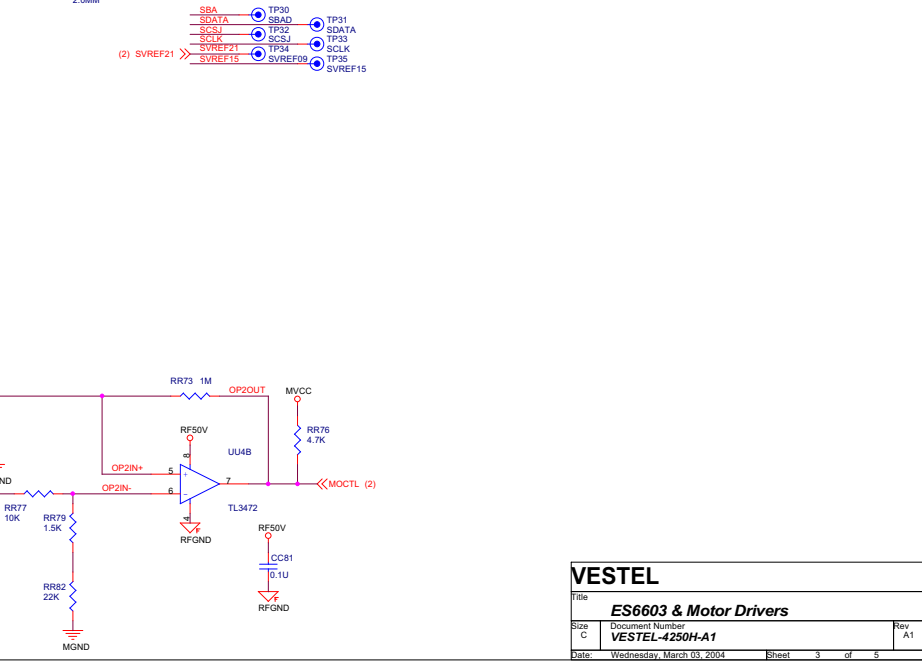
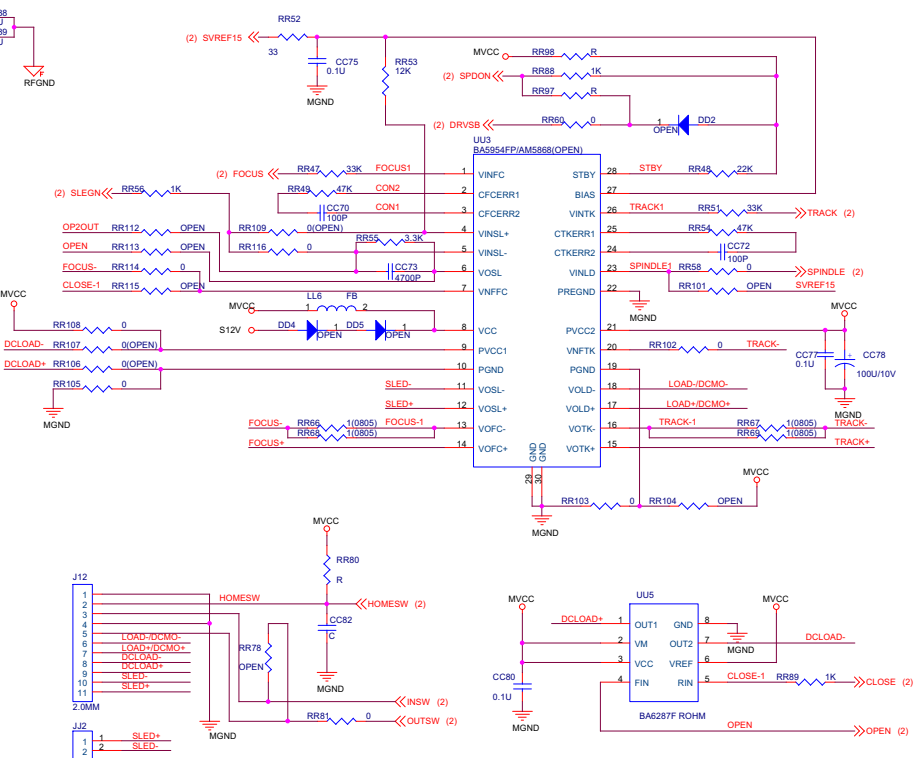
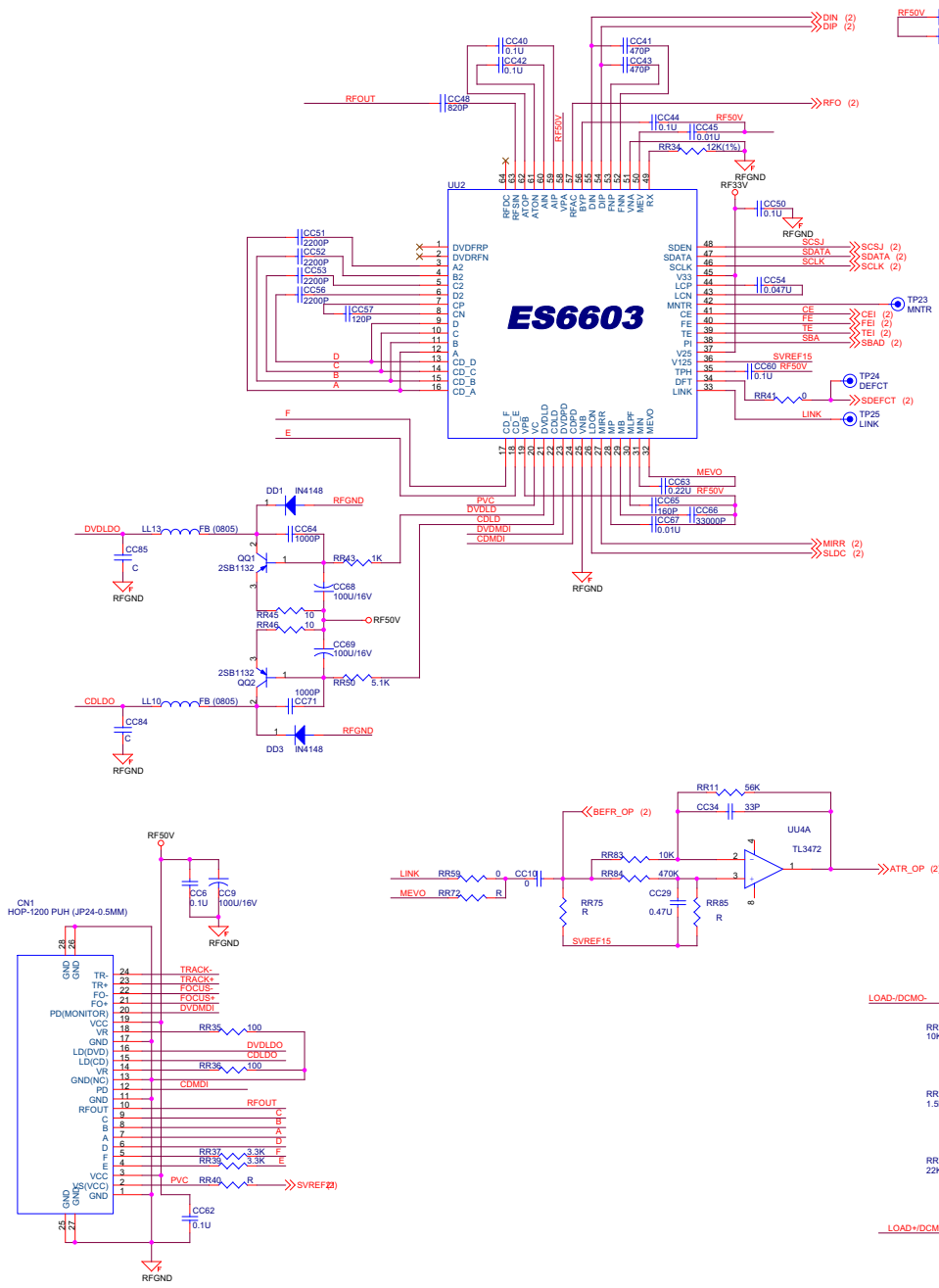
# Vibratto-II

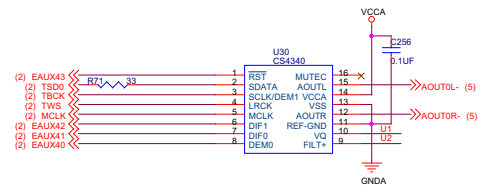
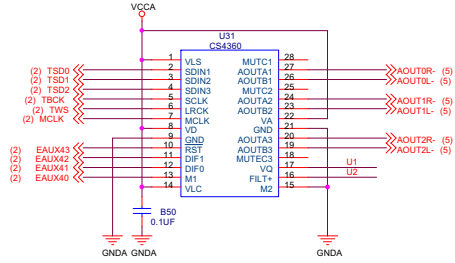
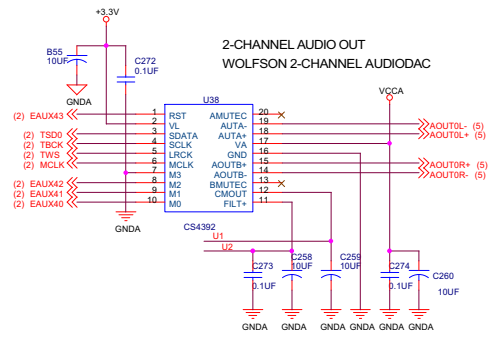
PLL2	PLL1	PLL0	DEFAULT	SCH-HP	DEFAULT	SCH-HP	Frequency
0	0	1	reserved	5	NA	135	
0	1	0	biases	biases	27	NA	
0	1	1	3.76	4	10125	108	
1	0	0	4.5	426	1215	114.75	
1	0	1	reserved	4.75	NA	120.25	
1	1	0	3.5	55	94.5	148.5	
1	1	1	4	6	108	102	

	CVBS = SVIDEO or CVBS + YUV	CVBS + YUV	SVIDEO + RGB	CVBS + RGB
VDMC	CVBS	CVBS	Y	CVBS
VDMC	Y	Y	G	G
CDAC	Y	Y	R	R
UDAC	U	U	B	B
FDAC	C	CVBS	C	CVBS

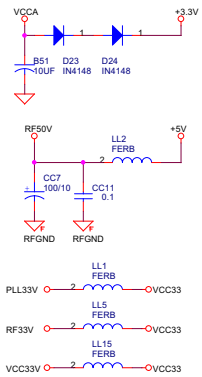
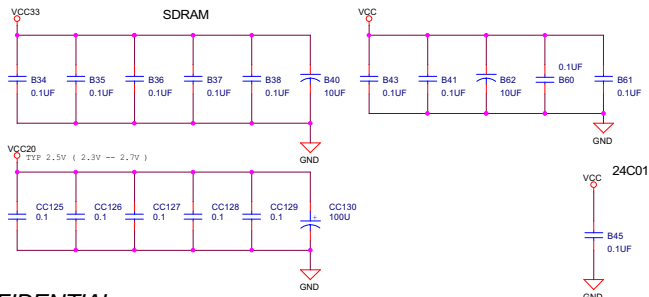
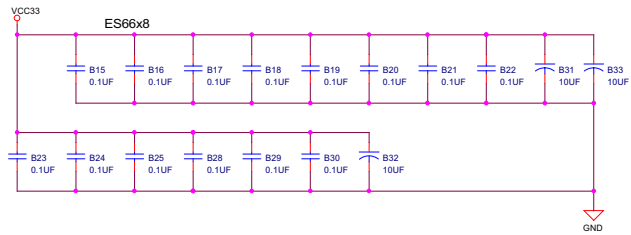
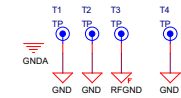
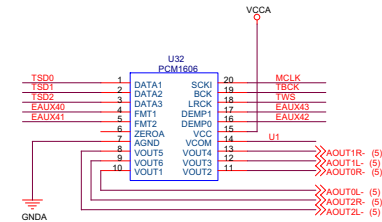


VESTEL	
Title	Vibratto-II ES66x8
Size	C
Document Number	VESTEL-4250H-A1
Date	Wednesday, March 03, 2004
Sheet	2 of 5
Rev	A1

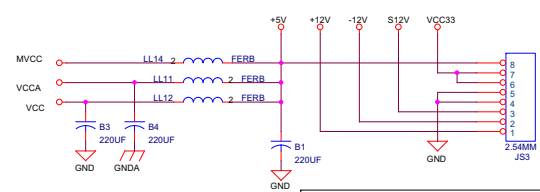
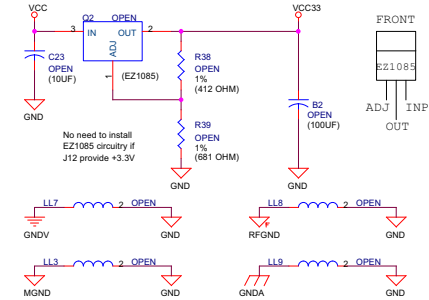




### 6-CHANNEL AUDIO OUT WOLFSON 6-CHANNEL AUDIODAC



### 3.3V REGULATOR

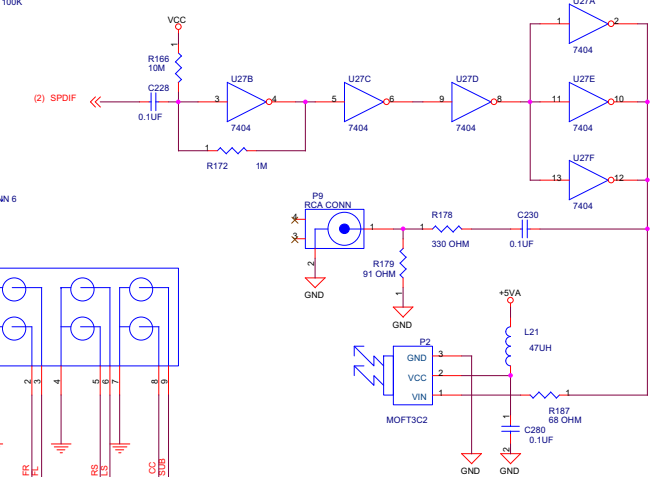
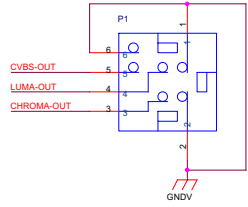
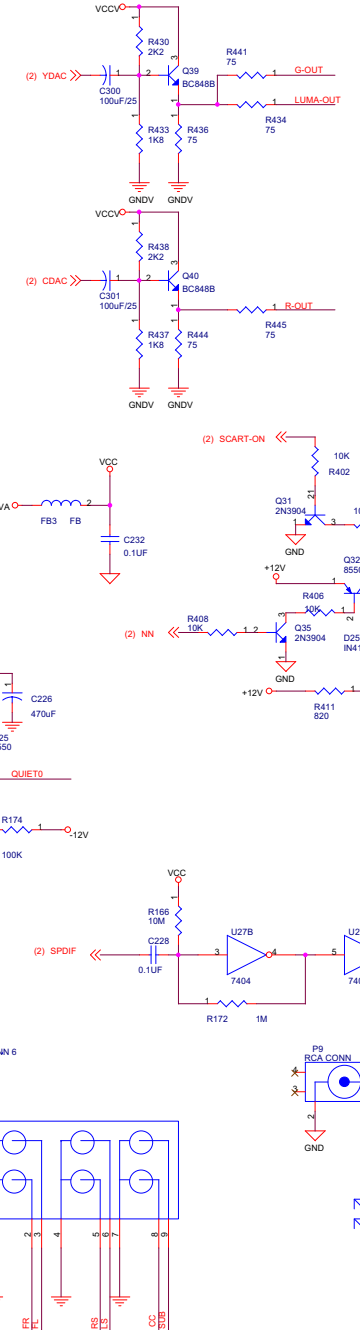
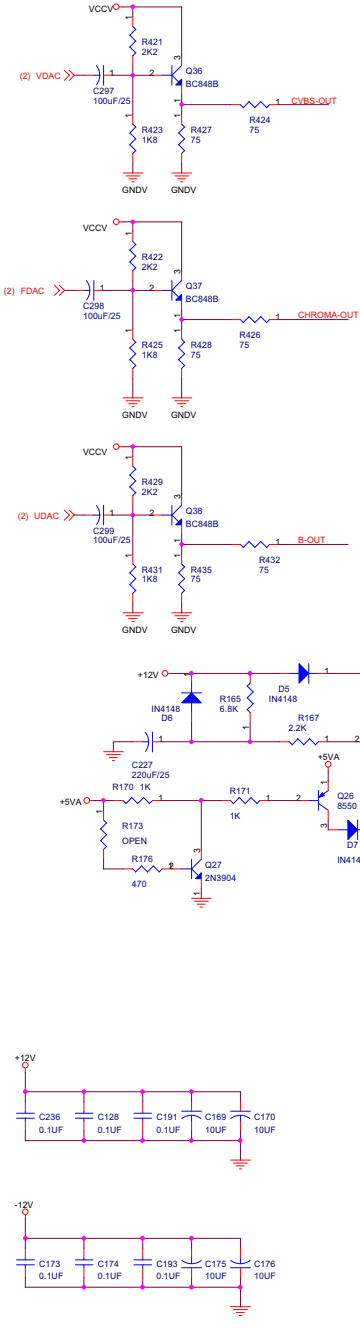
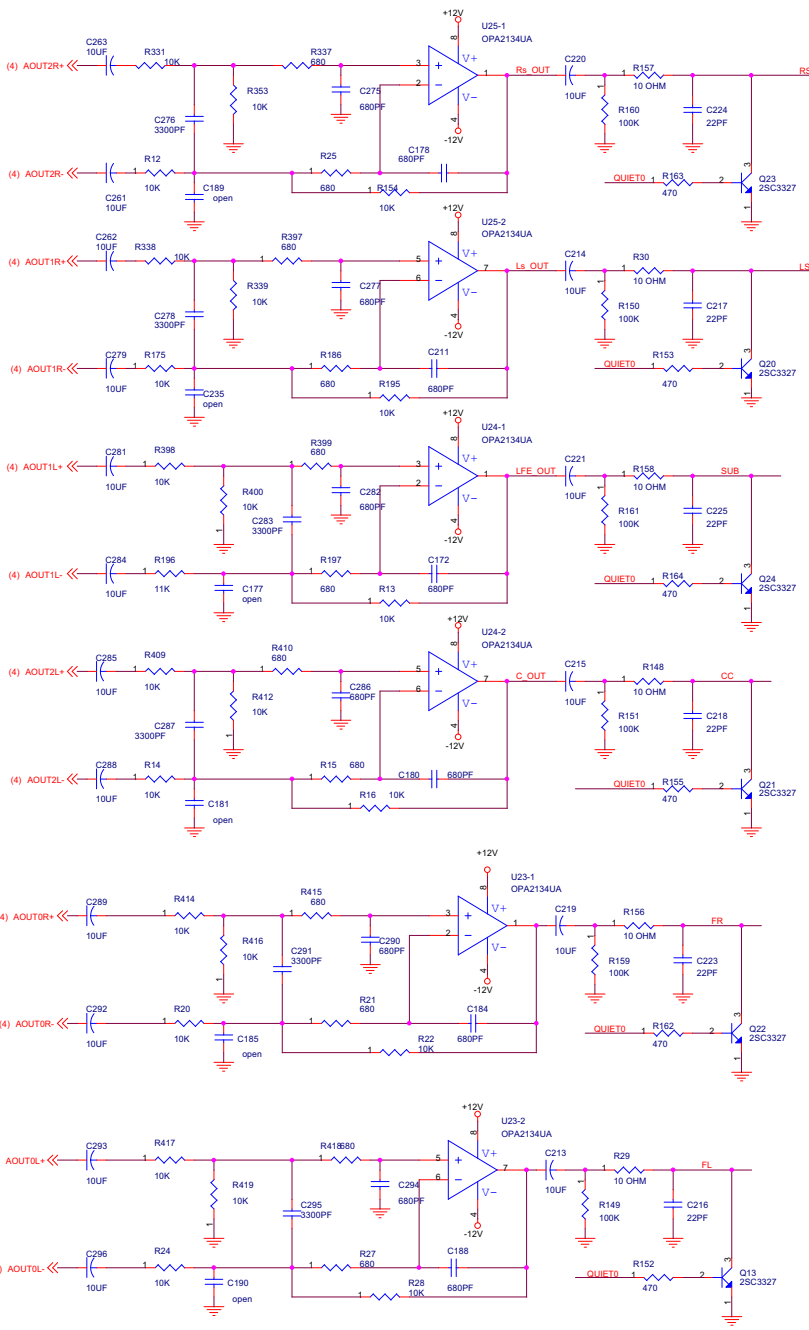


**ESS CONFIDENTIAL**

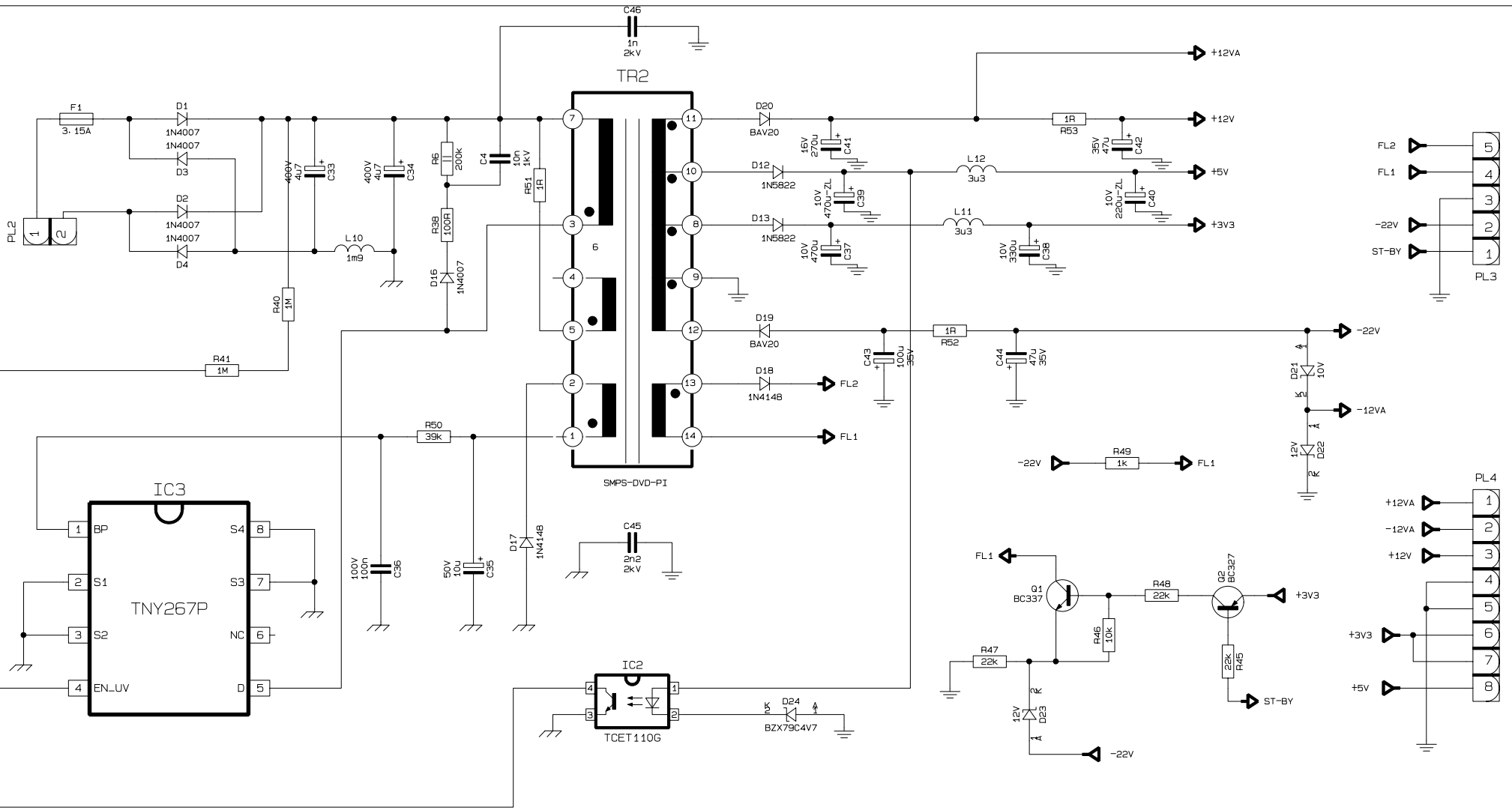
The information has been checked and is believed to be reliable. However, no responsibility is assumed for inaccuracies. Circuit diagrams are provided as a means of illustrating typical applications; consequently complete information for construction purposes is not necessarily given. ESS reserves the right to make changes at any time in order to improve the design.

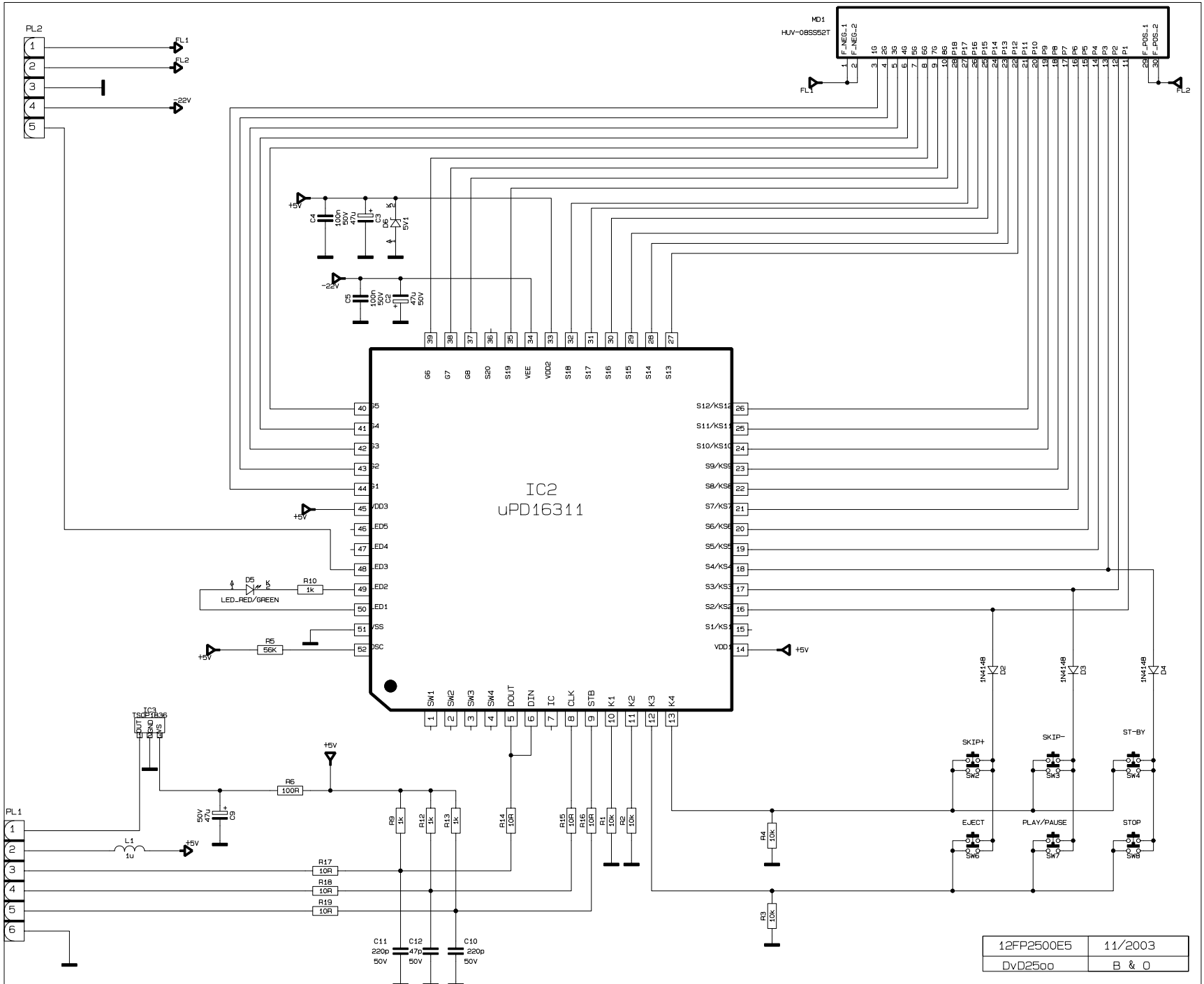
**VESTEL**

File	MISC		Rev	A1
Size	Document Number		VESTEL-4250H-A1	
C	Date		Wednesday, March 03, 2004	
	Sheet		4 of 5	



<b>VESTEL</b>		
File	<b>OUTPUT</b>	
Size	Document Number	Rev
C	<b>VESTEL-4250H-A1</b>	A1
Date	Wednesday, March 03, 2004	Sheet 5 of 5





12FP2500E5	11/2003
DvD2500	B & O