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**KTL AUTOMATIZALASI TERV  
MUNKAPELDANY**

Author: BERESS GYORGY

000

File:

Date: 2017.07.05.

Folio: 1/23

	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17
A																	
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C																	
D	<p>The diagram in row D shows two circuit components. On the left, there is a parallel combination of a resistor and a capacitor. The resistor is represented by a rectangle with a diagonal line from the bottom-left to the top-right, and the number '12' is written vertically inside it. The capacitor is represented by two parallel vertical lines. On the right, there is an inductor represented by a vertical zigzag line.</p>																
E																	
F																	
G																	
H																	

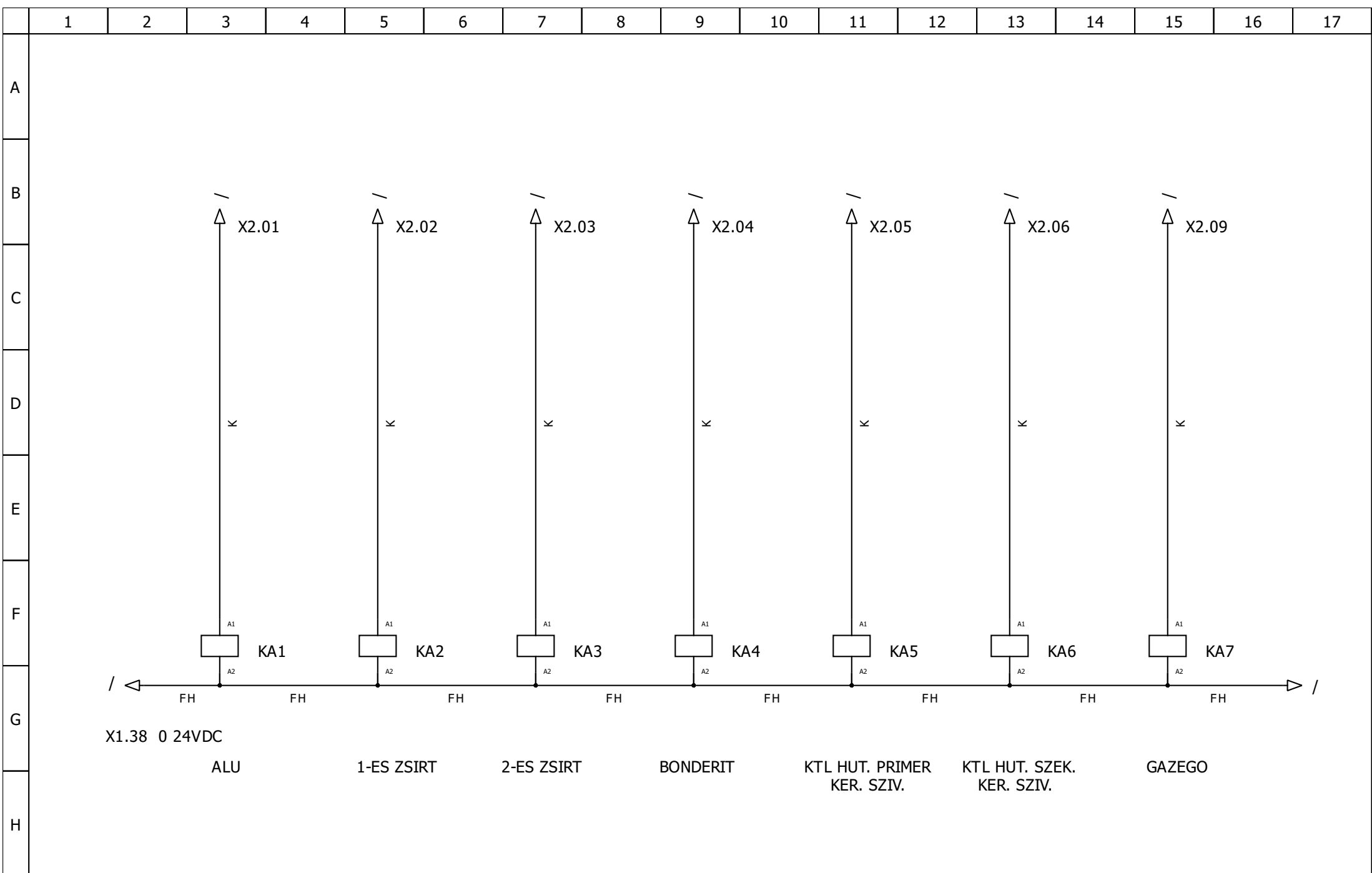
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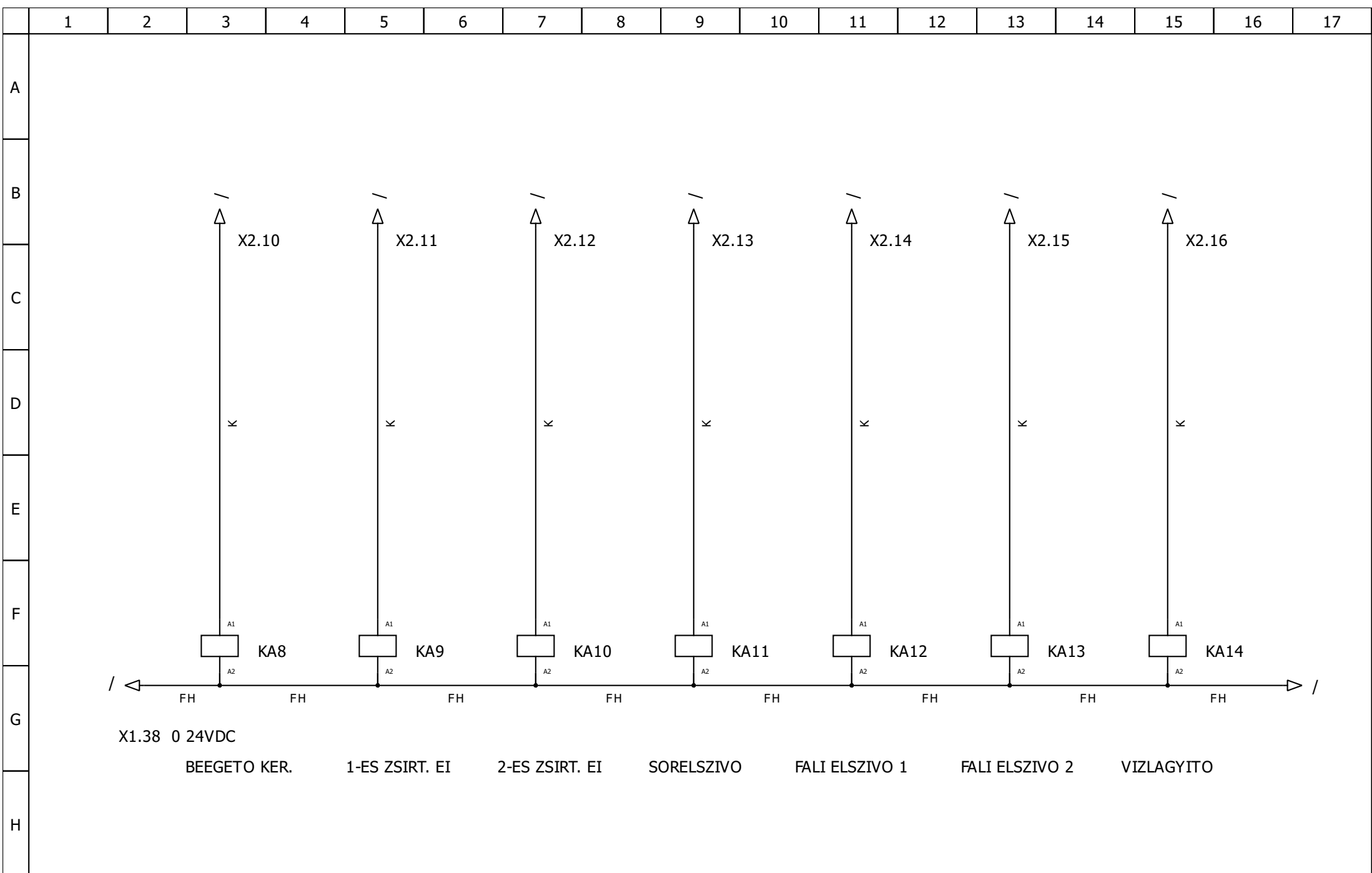
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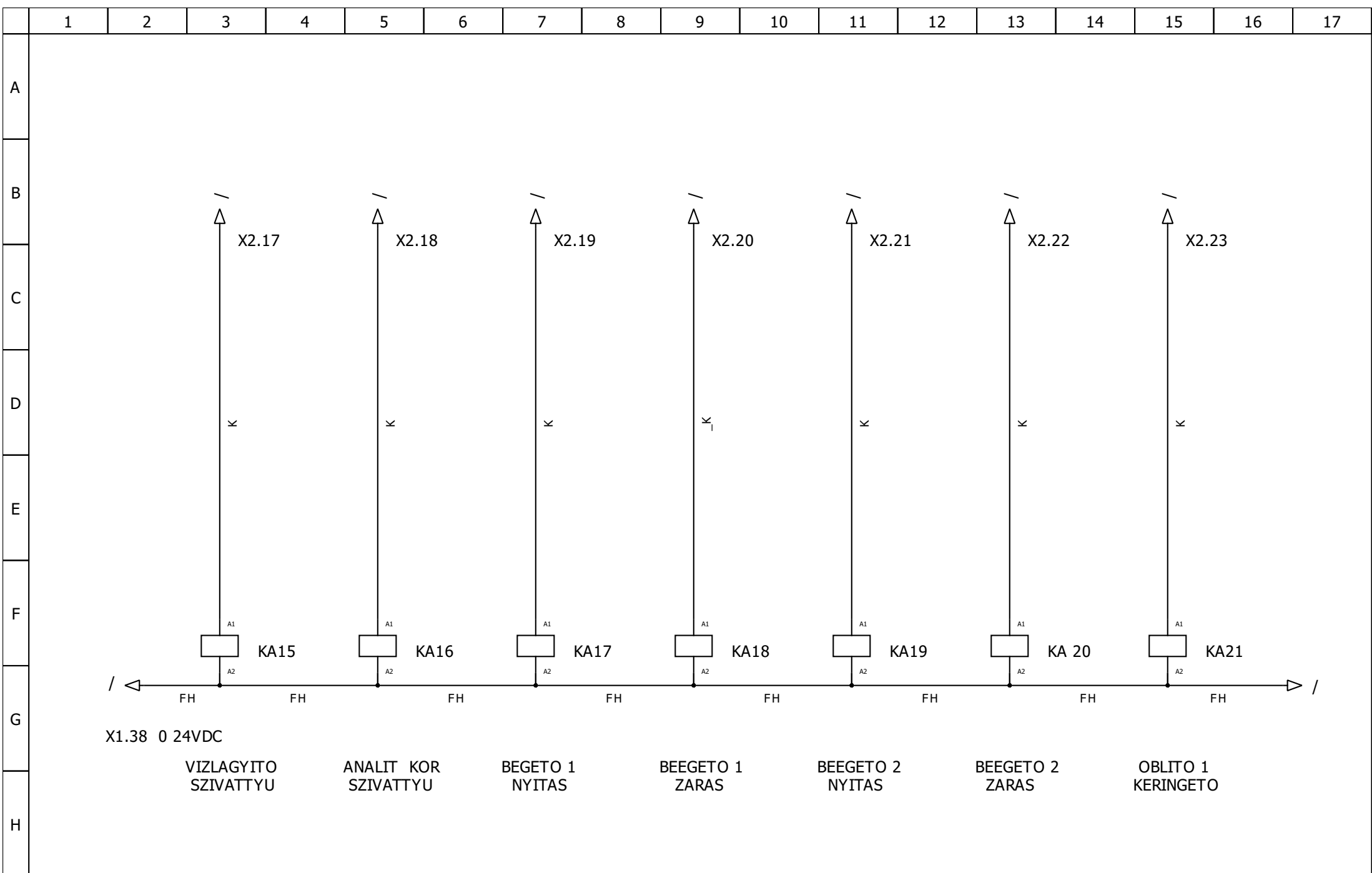
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2

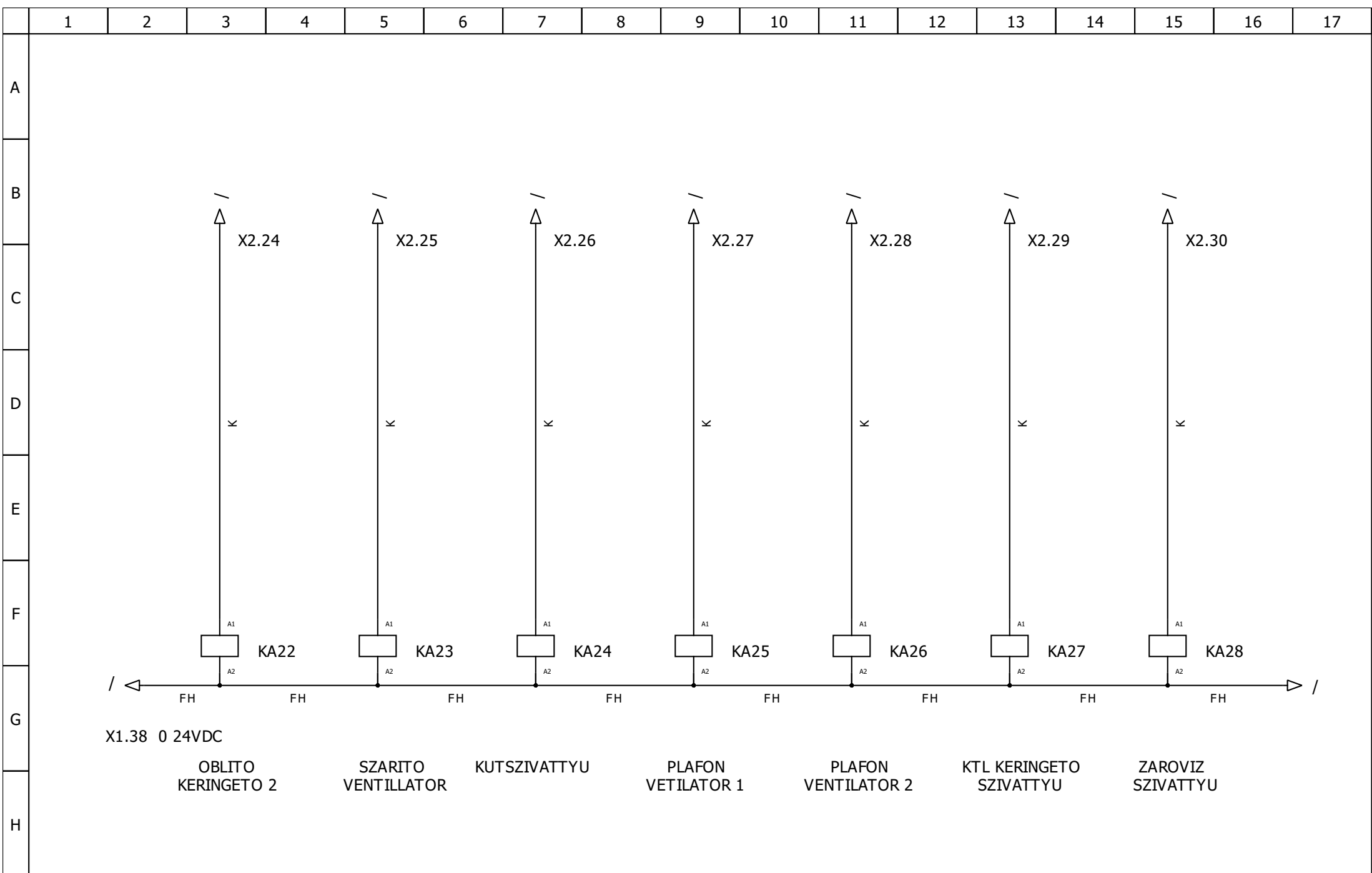
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Folio: 4/23



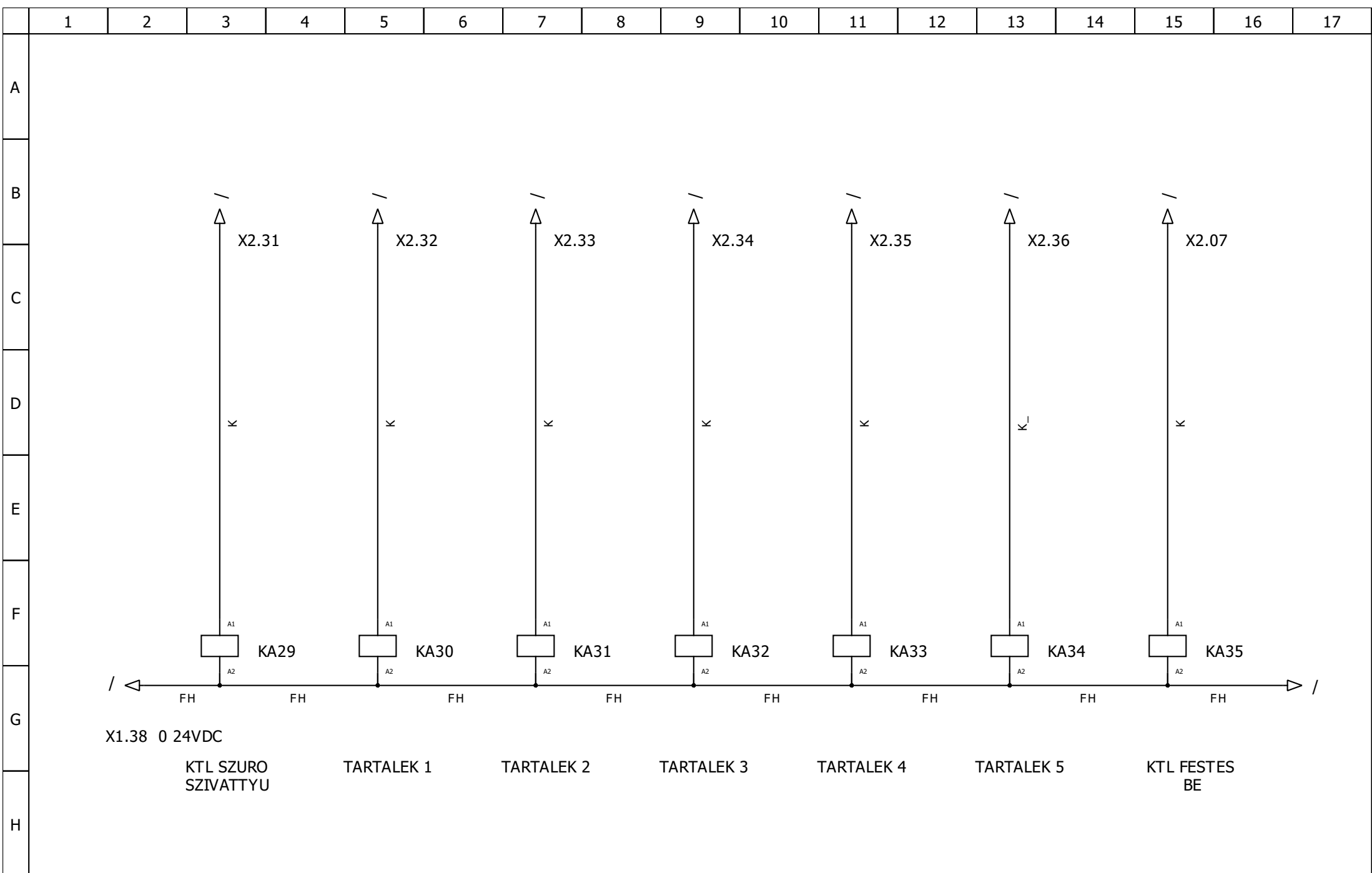
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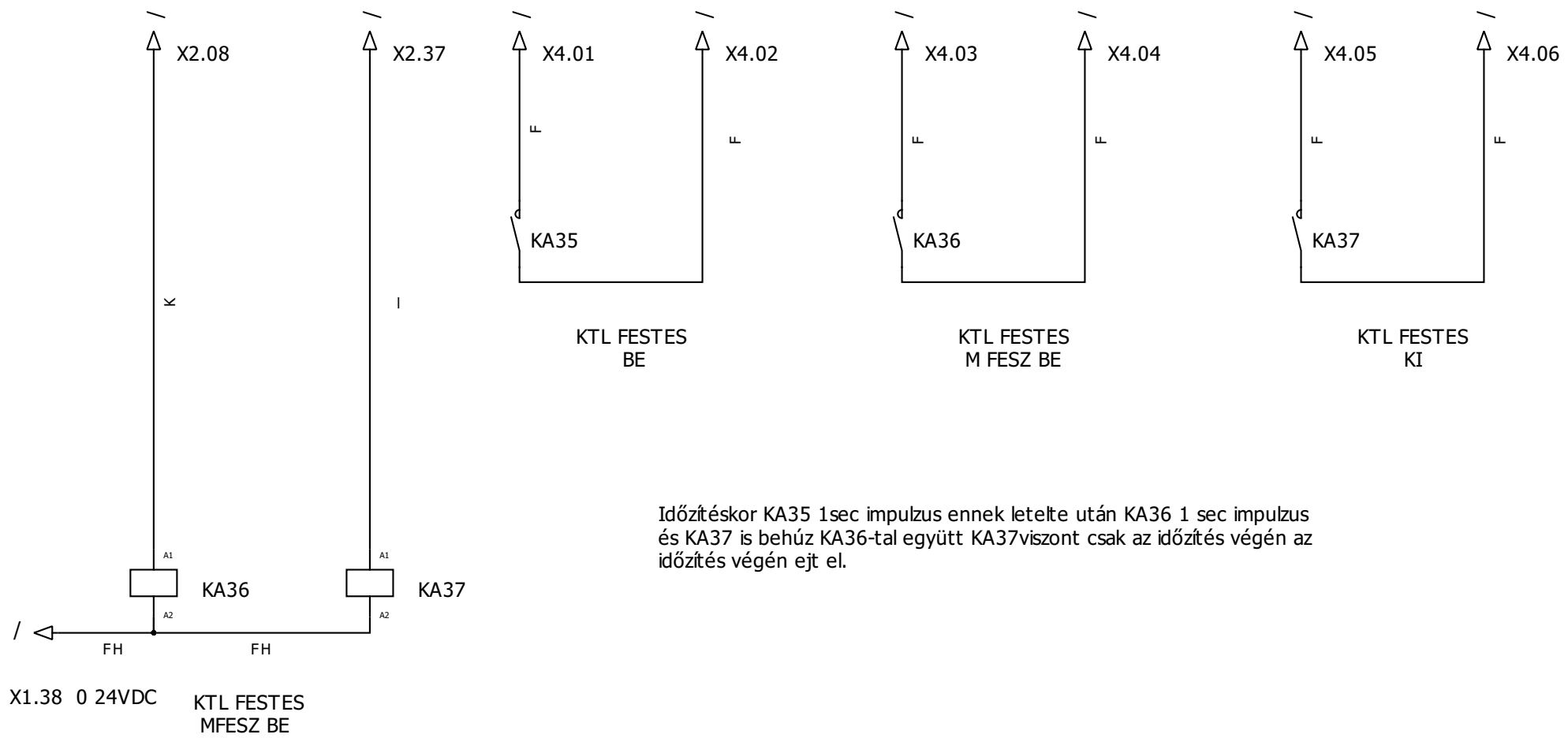
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Date:

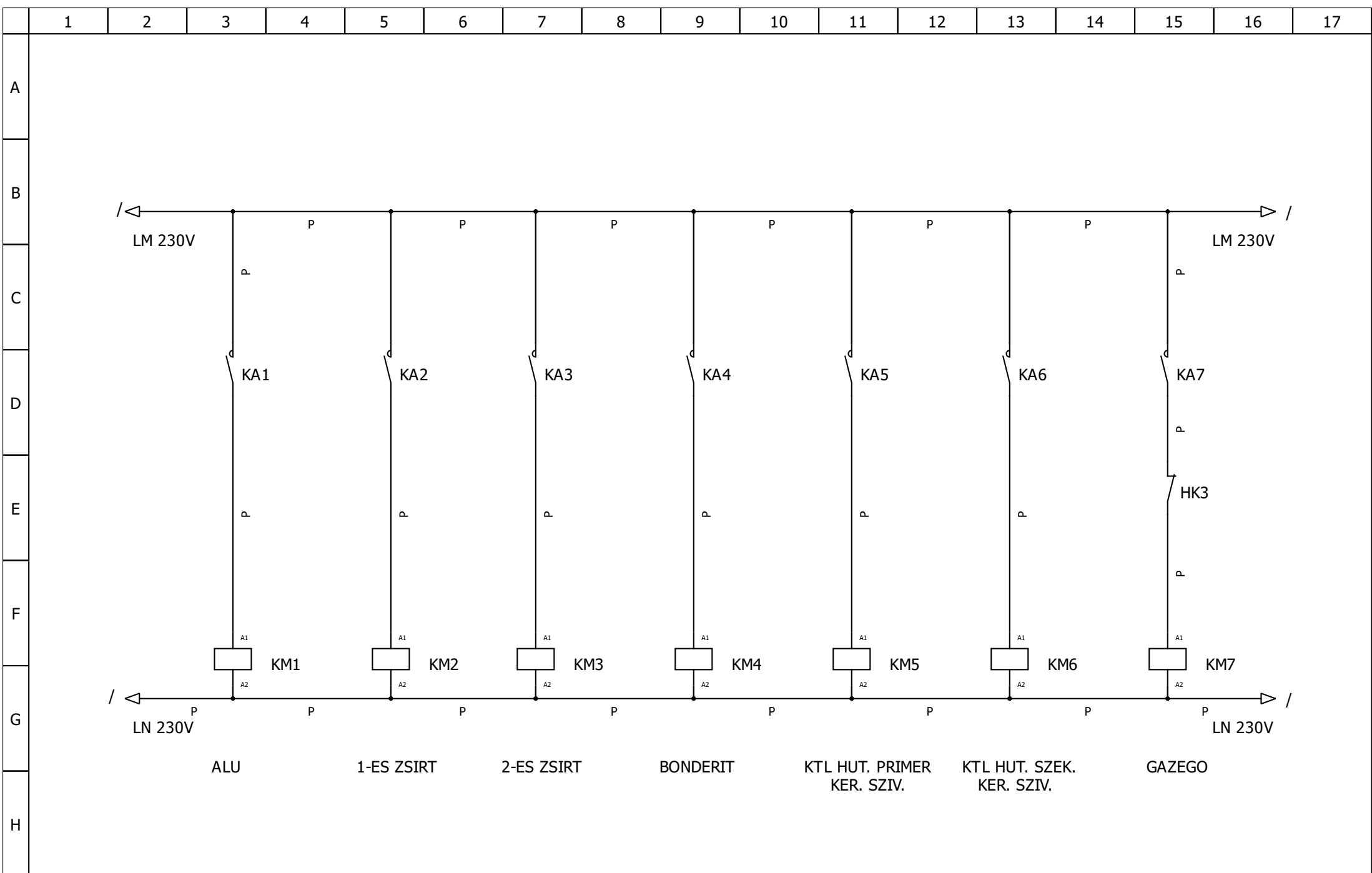


Author:  
Date:



Időzítéskor KA35 1sec impulzus ennek letelte után KA36 1 sec impulzus és KA37 is behúz KA36-tal együtt KA37vizont csak az időzítés végén az időzítés végén ejt el.





Author:

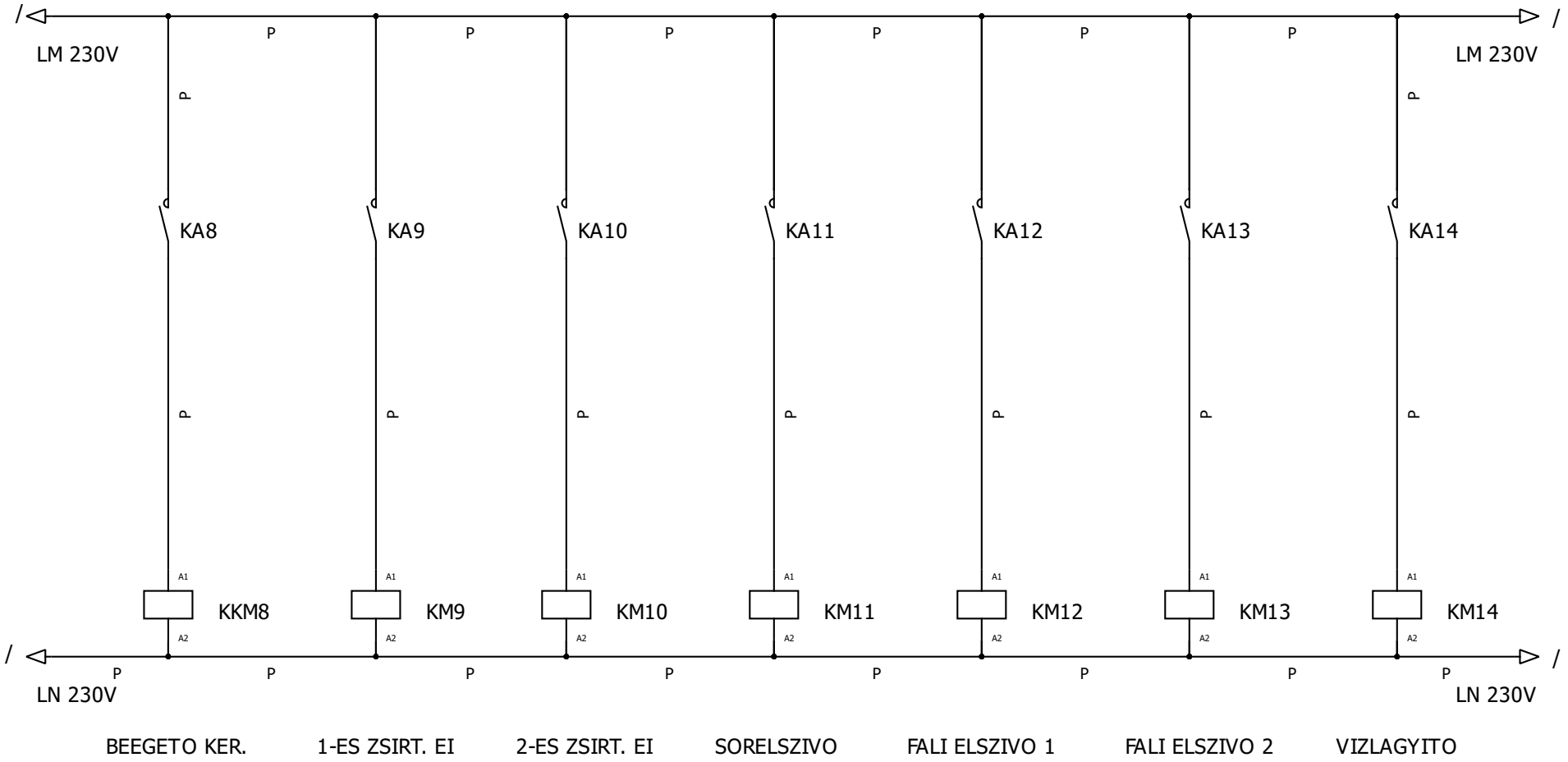
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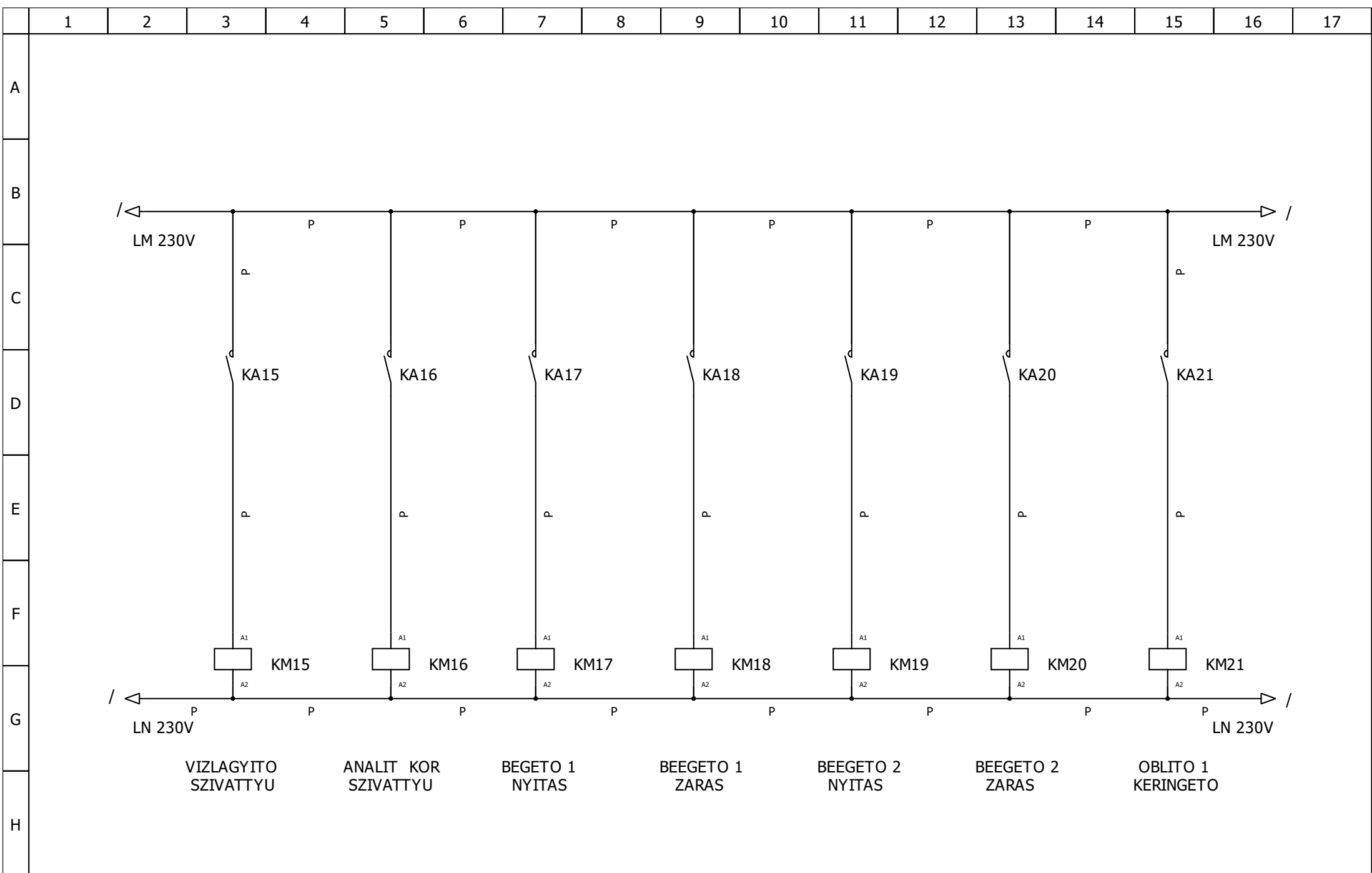
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Folio: 9/23

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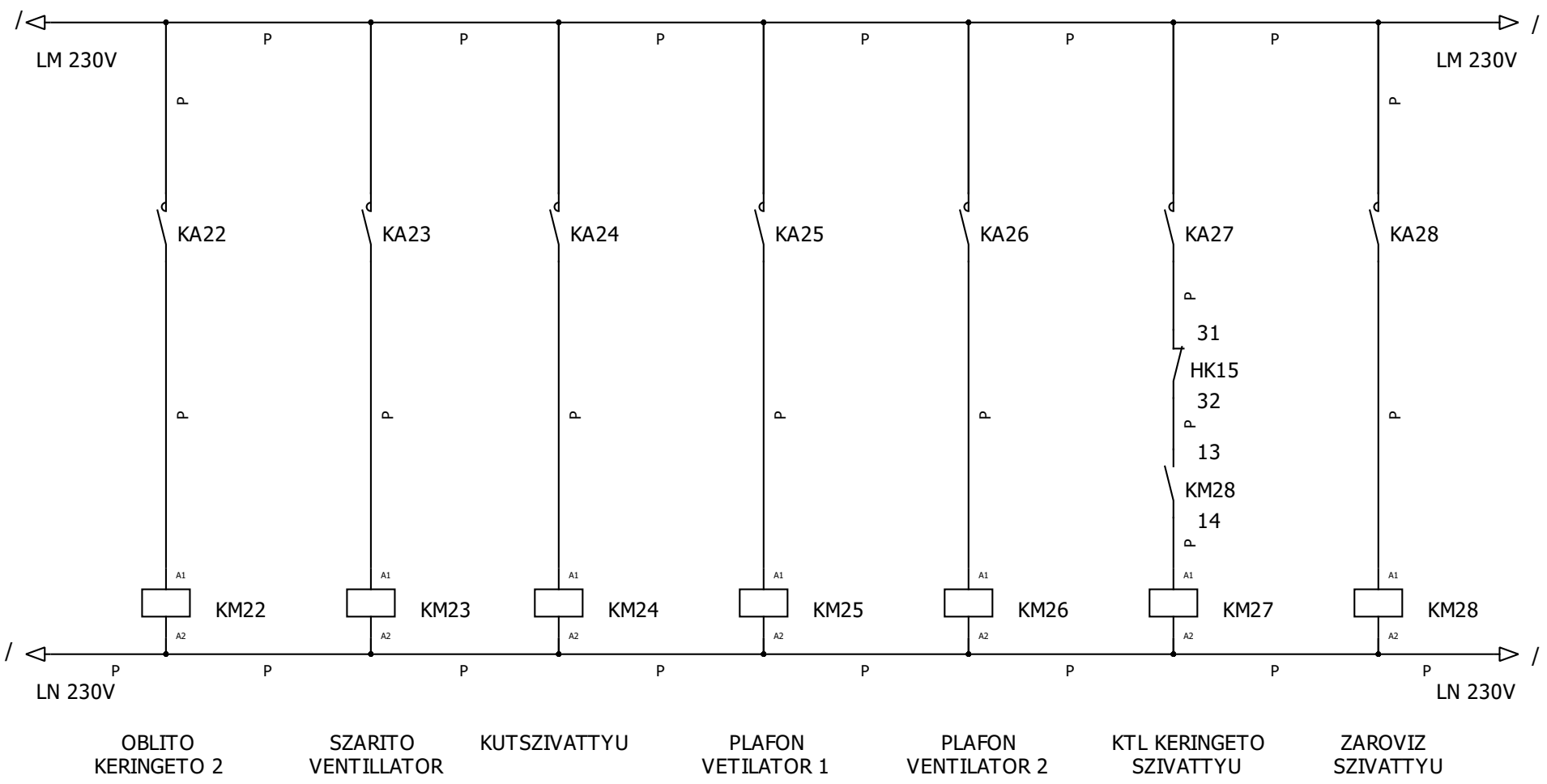


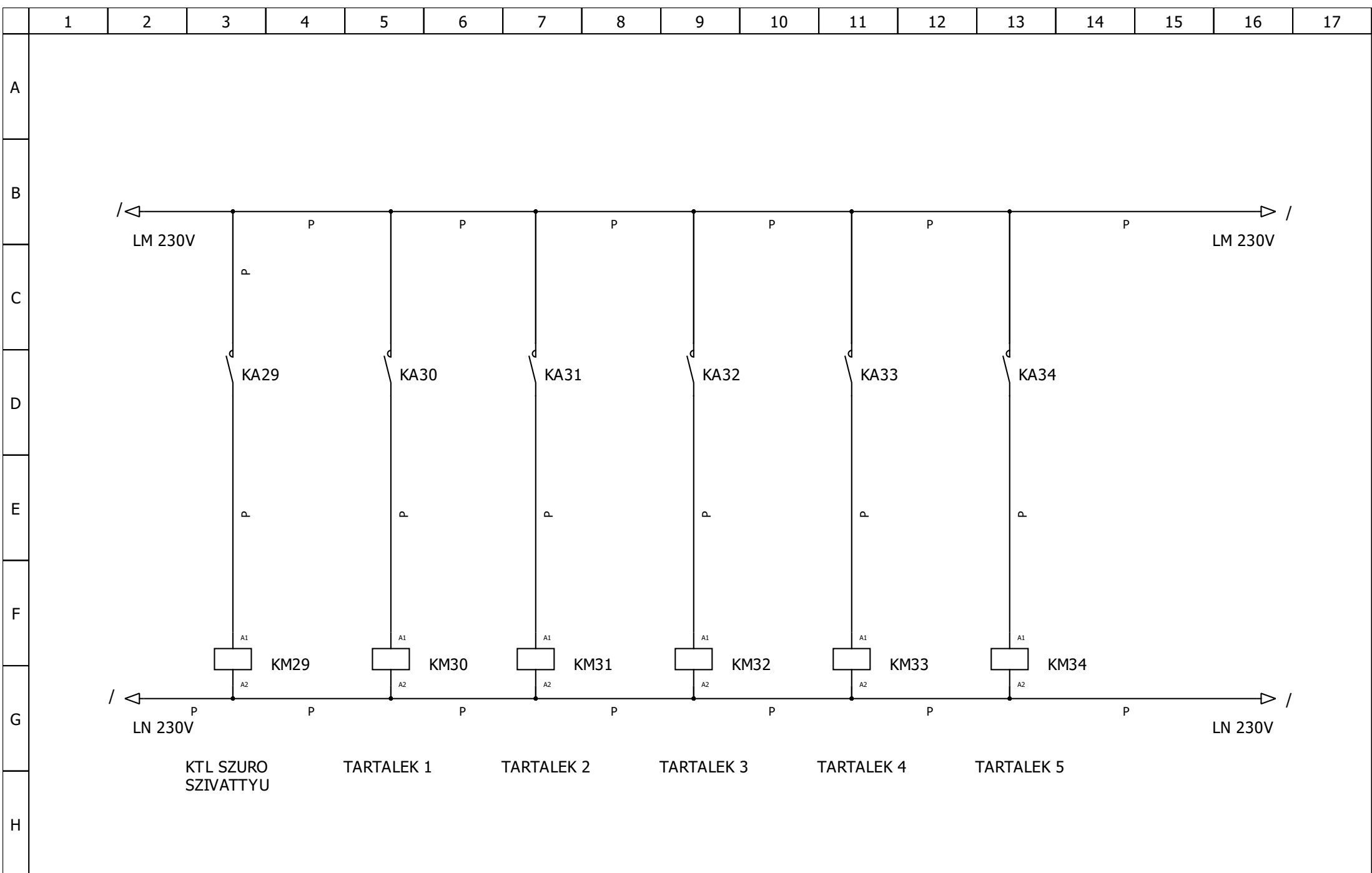


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3A

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Folio: 11/23





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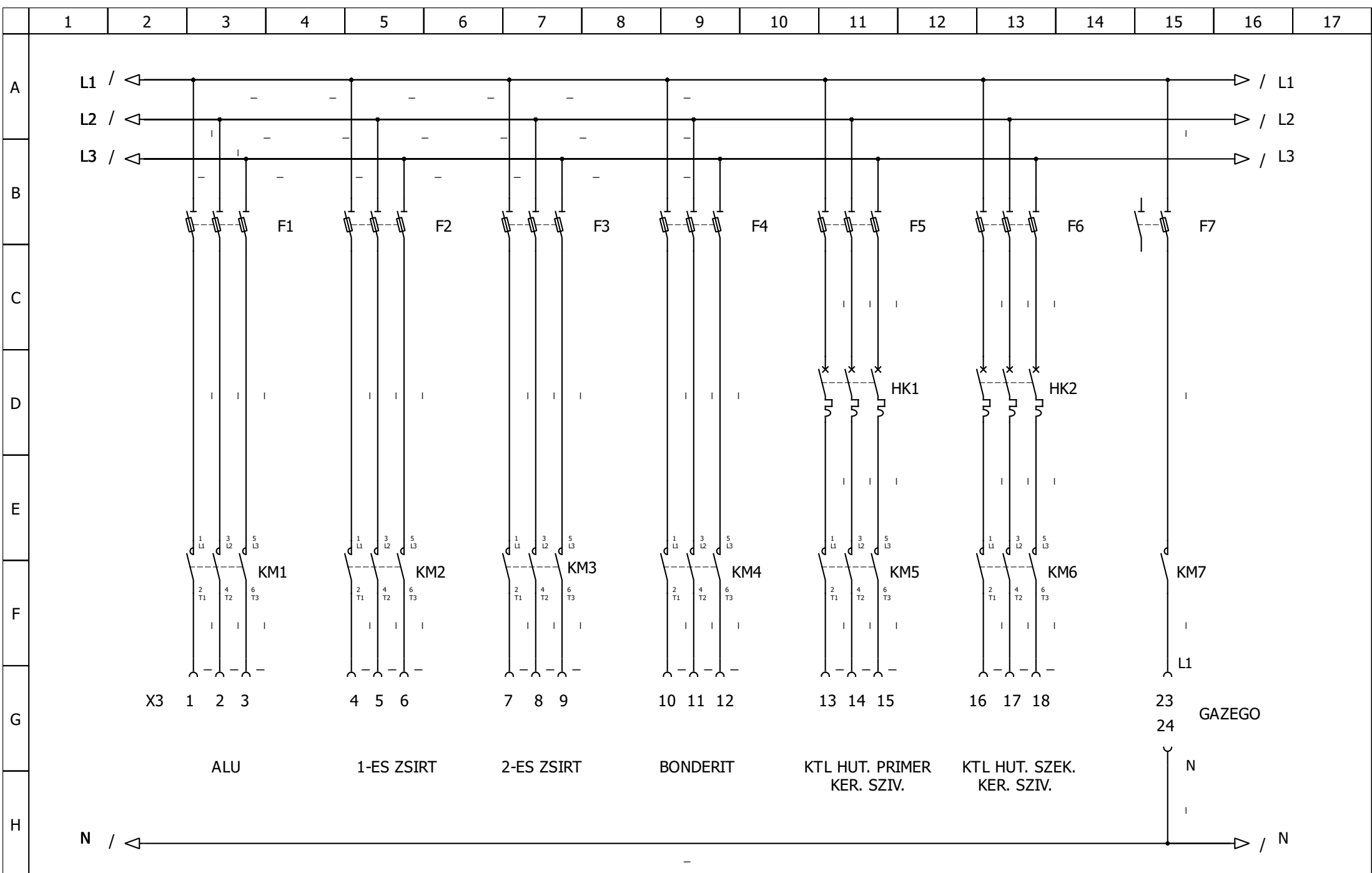
5A

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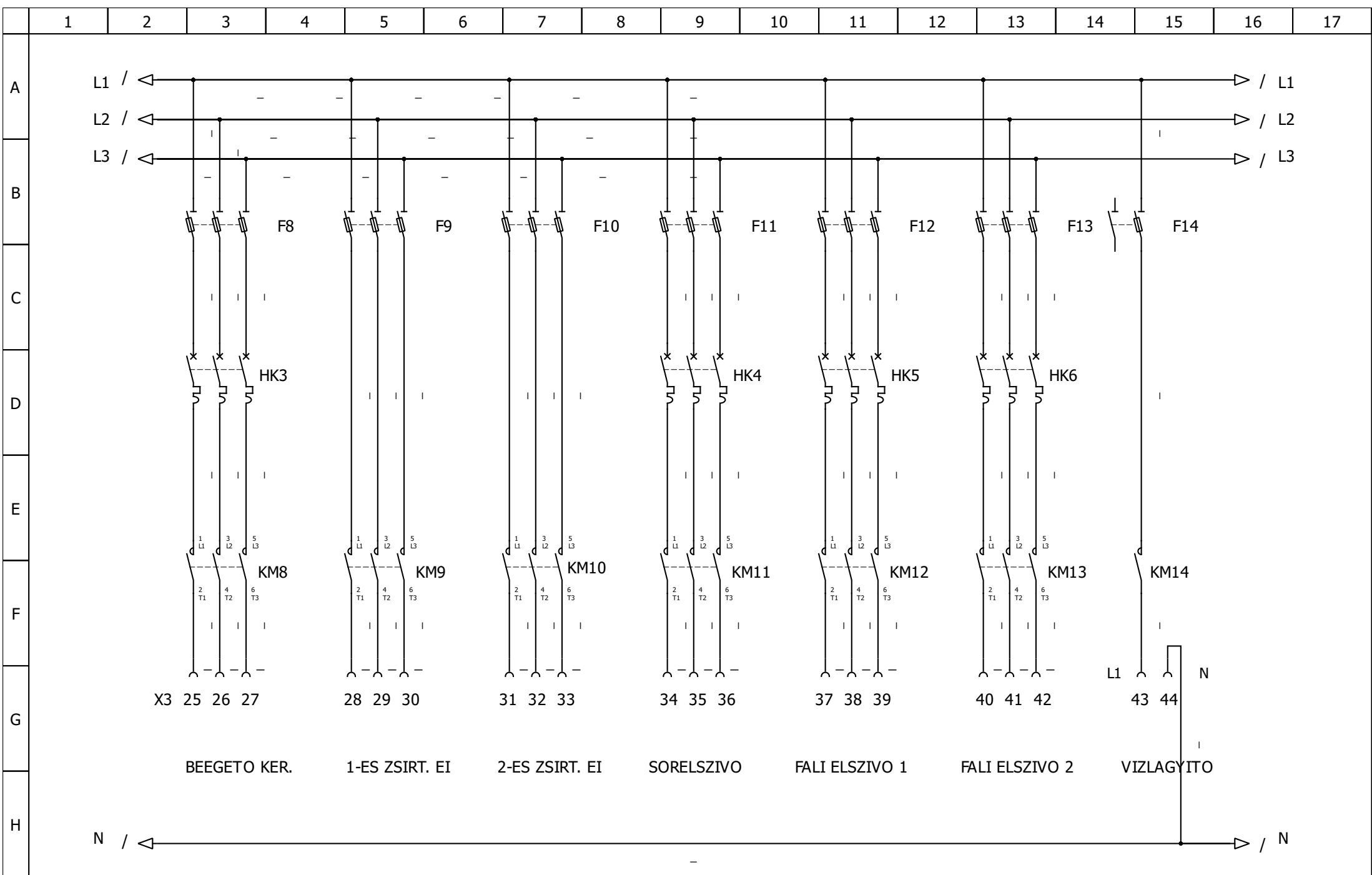
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1B

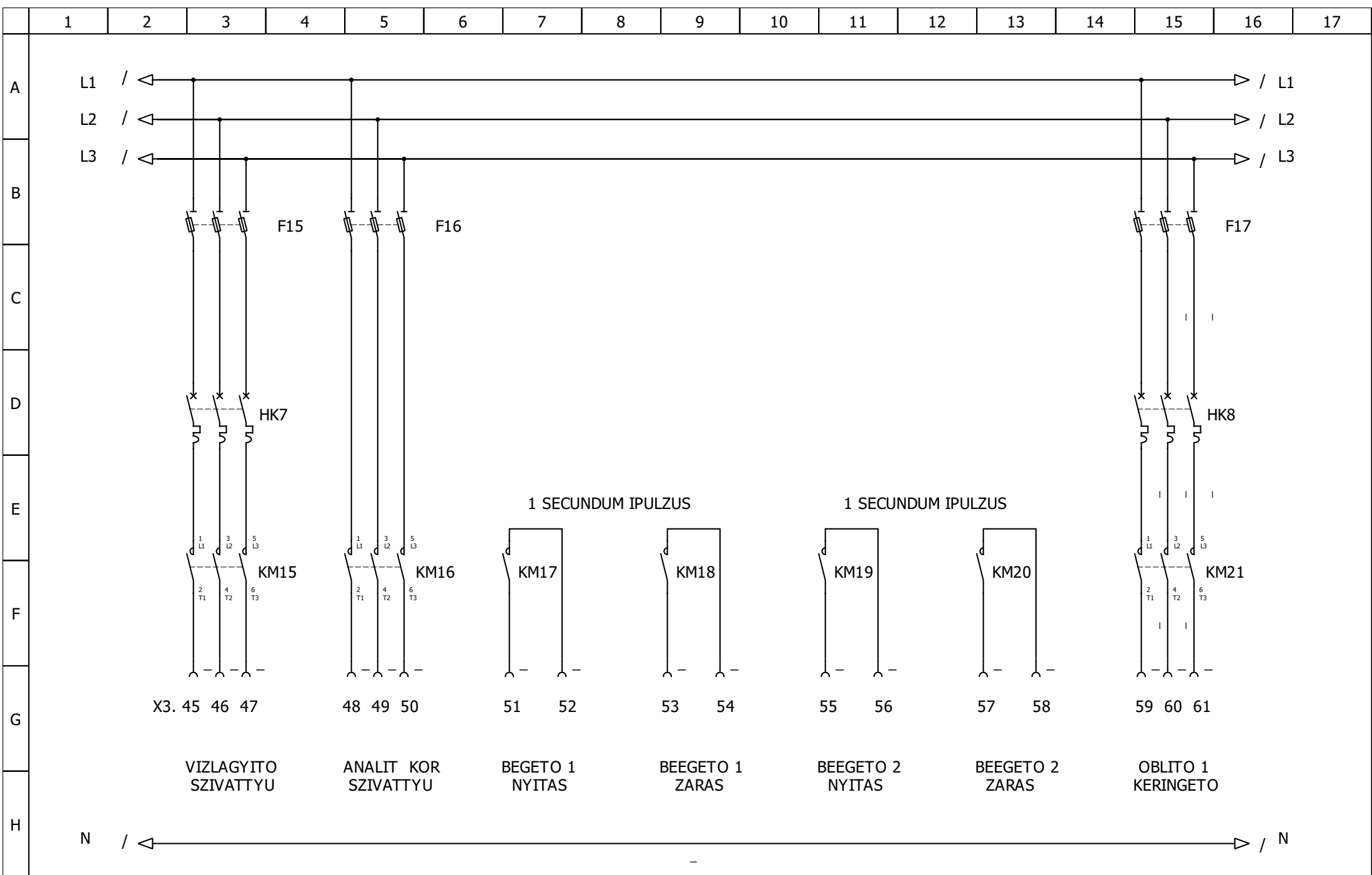
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Folio: 14/23



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File:  
Folio: 15/23

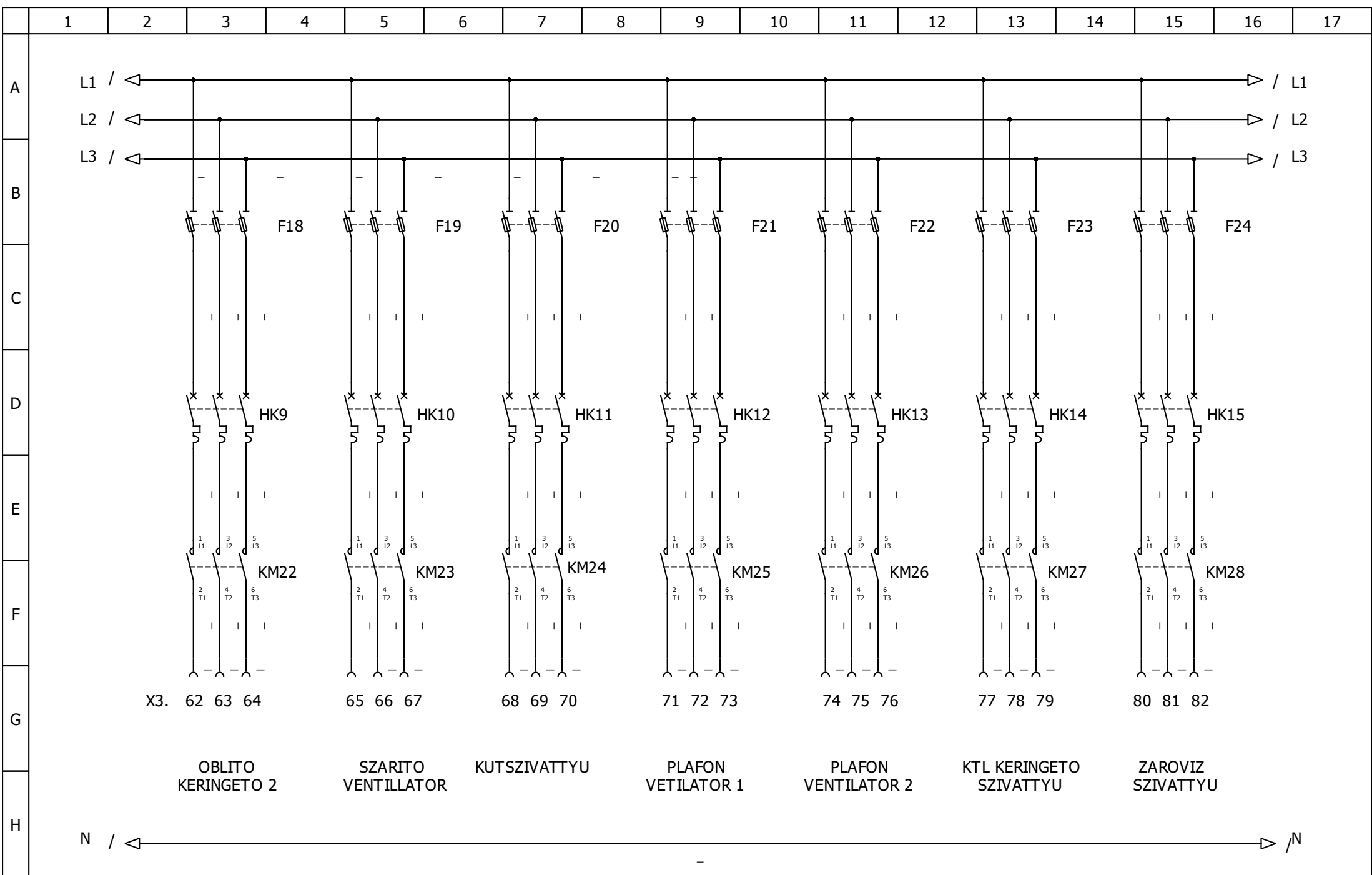


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Folio: 16/23

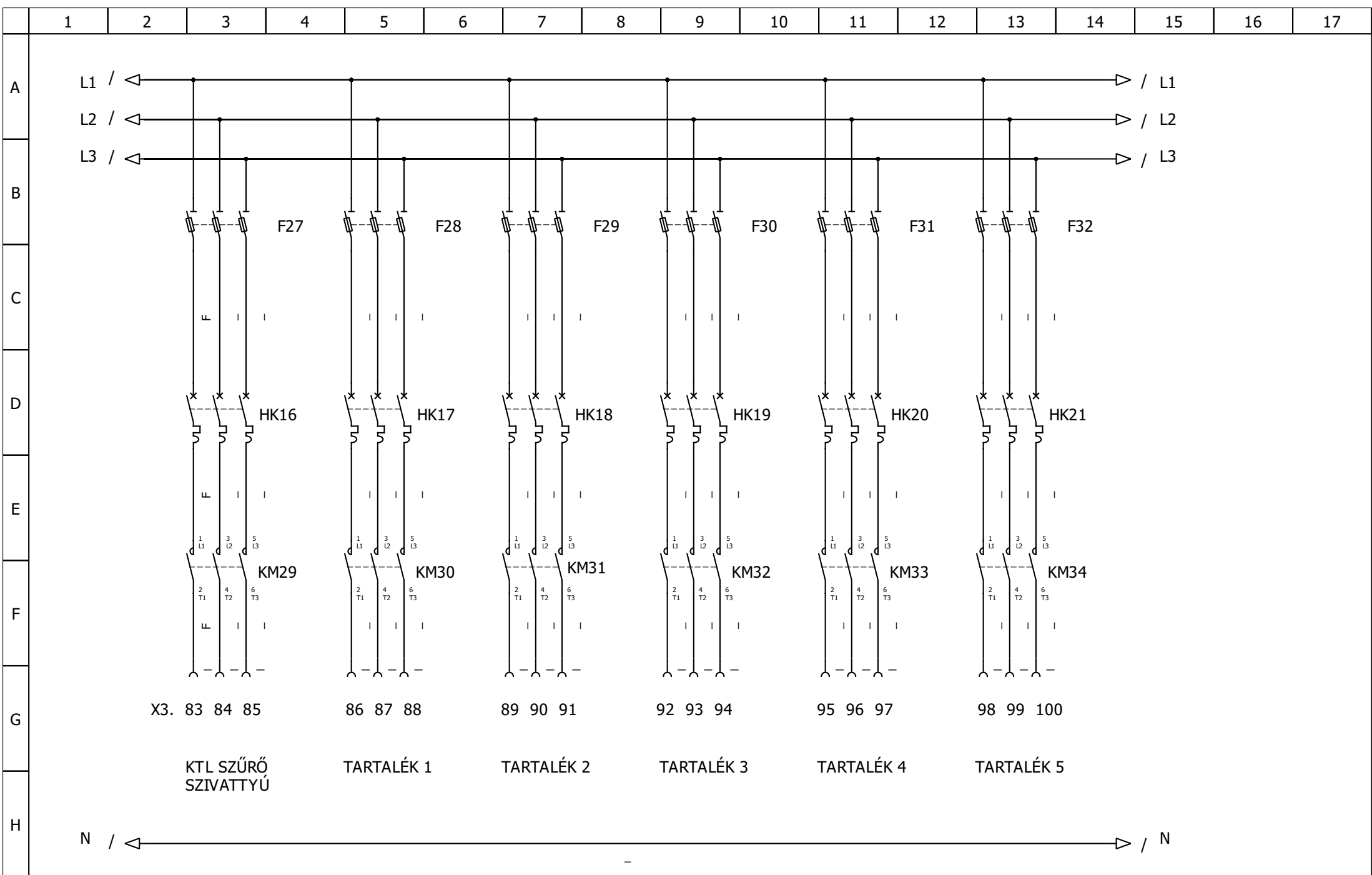




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4B

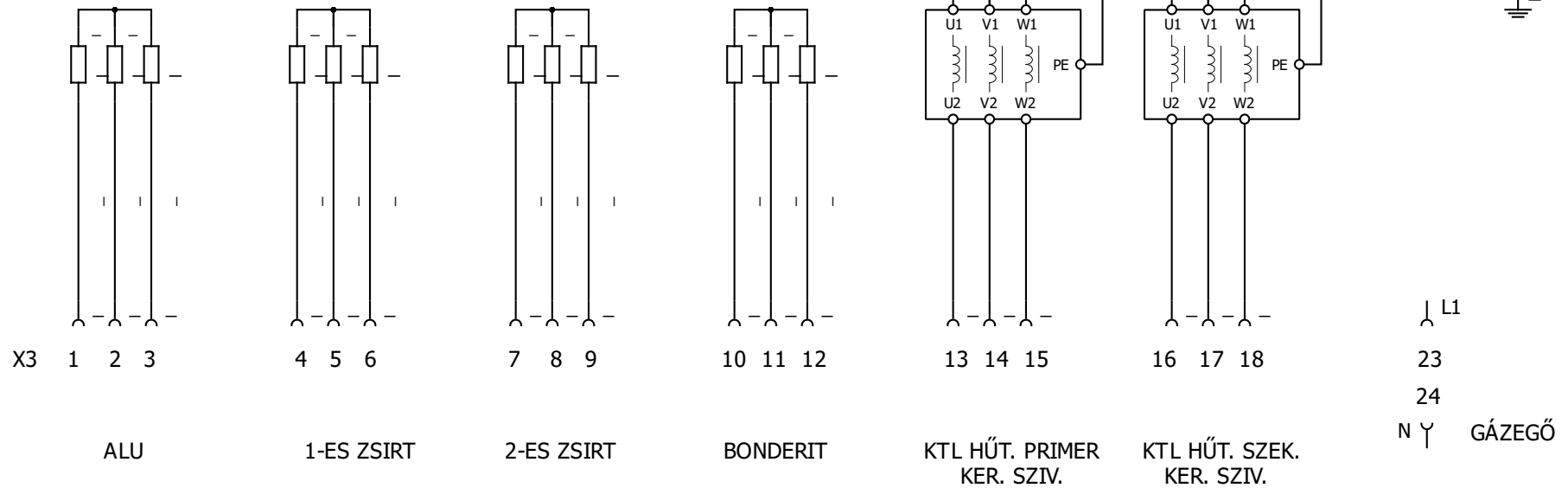
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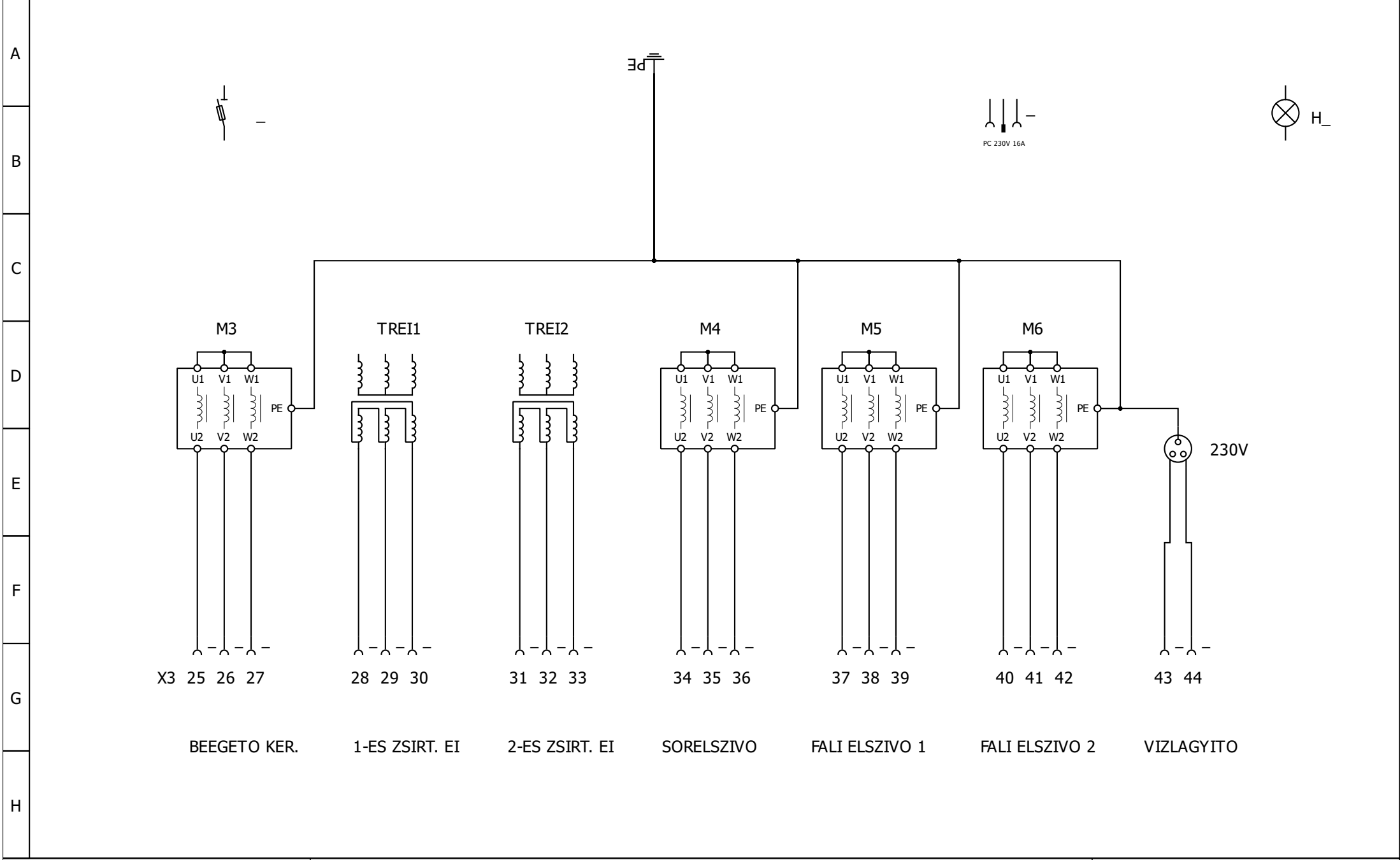


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Date:

5B

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Folio: 18/23





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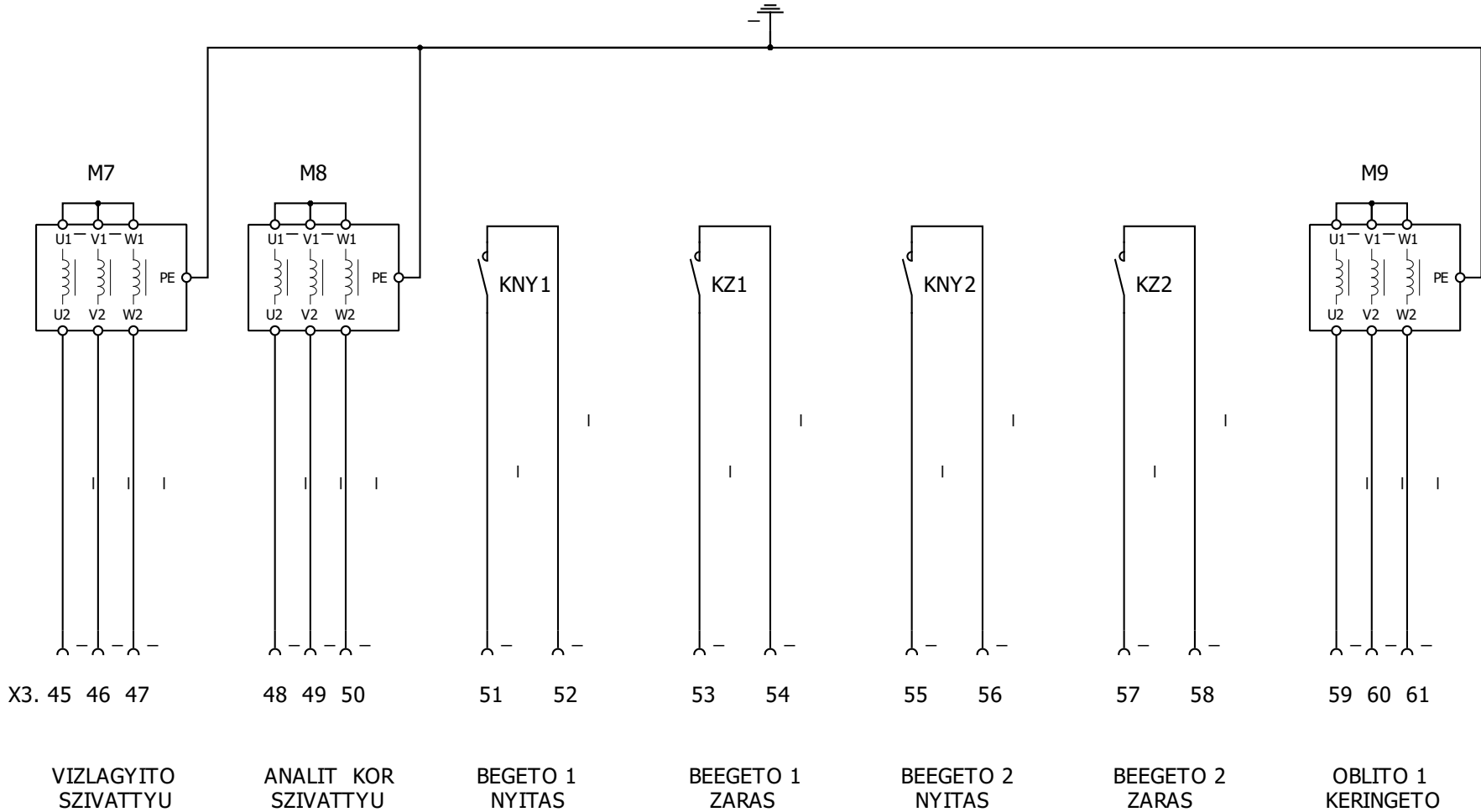
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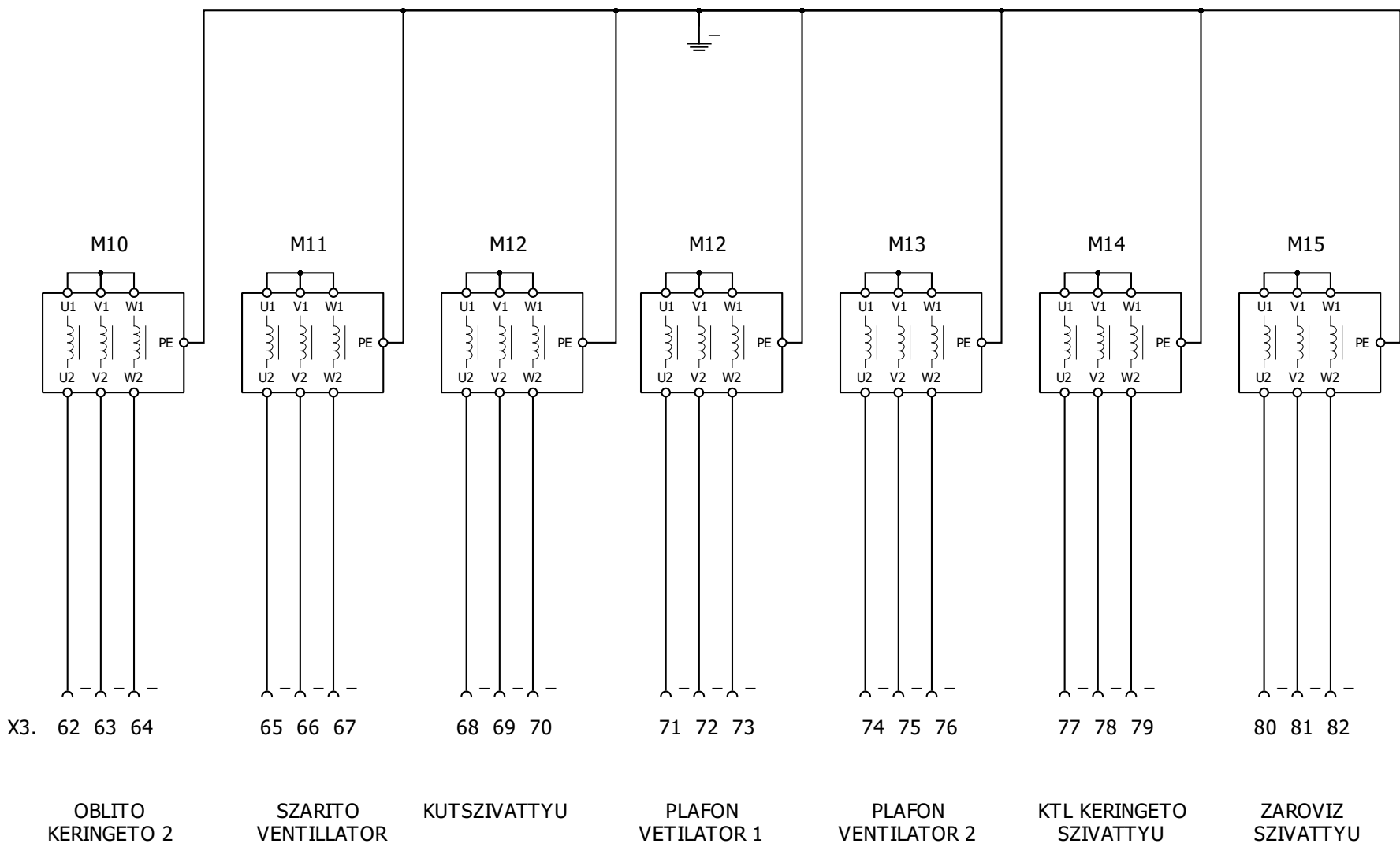
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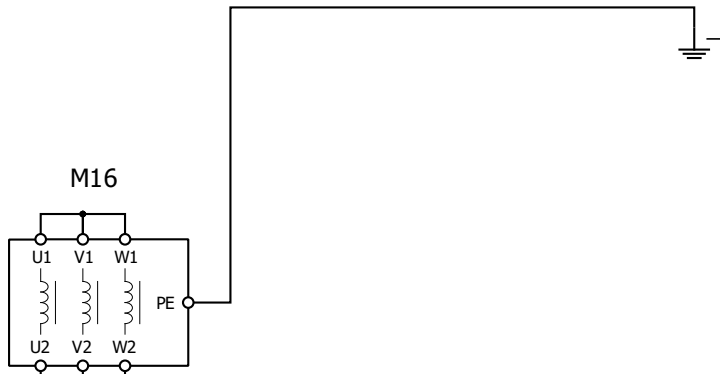
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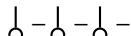
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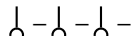
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KTL SZURO SZIVATTYU



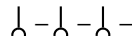
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TARTALEK 1



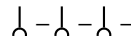
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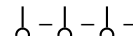
92 93 94

TARTALEK 3



95 96 97

TARTALEK 4



98 99 100

TARTALEK 5

Author:

Date:

5C

File:

Folio: 23/23