LB1989



Three-Phase Sensorless VCR Drum Motor Driver

## **Functions and Features**

- · Soft switching drive
- No Hall sensors required.
- No FG sensors required.
- Built-in PG amplifier
- Thermal shutdown circuit
- Current limiter circuit

## **Package Dimensions**

unit: mm

#### 3222-HSOP28



## **Specifications**

#### Absolute Maximum Ratings at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V <sub>CC</sub> max		14.5	V
Maximum output voltage	V <sub>O</sub> max		14.5	V
Maximum input voltage	V <sub>I1</sub> max		-0.3 to V <sub>CC</sub> 1 + 0.3	V
Maximum cylinder current	l <sub>O</sub> max		1.0	A
Allowable power dissipation	Pdmax	Independent IC	0.6	W
Operating temperature	Topr		-20 to +75	°C
Storage temperature	Tstg		-55 to +150	°C

#### Allowable Operating Ranges at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Supply voltage	V <sub>CC</sub>		8 to 13.8	V

- Any and all SANYO products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your SANYO representative nearest you before using any SANYO products described or contained herein in such applications.
- SANYO assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all SANYO products described or contained herein.

SANYO Electric Co., Ltd. Semiconductor Company TOKYO OFFICE Tokyo Bldg., 1-10, 1 Chome, Ueno, Taito-ku, TOKYO, 110-8534 JAPAN

# Electrical Characteristics at Ta = 25°C, $V_{CC}$ = 12 V

	Symbol			Ratings			
Parameter		Conditions	min	typ	max	Unit	l est circuit
Current drain	I <sub>CC</sub>	$V_{\rm C} = 0 \ V$		15	20	mA	1
Internal power supply	V <sub>REG</sub>	V <sub>C</sub> = 0 V	4.6	5.0	5.4	V	2
Output saturation voltage 1	V <sub>OSAT</sub> 1	I <sub>O</sub> = 0.4 A, Source + Sink		1.4	2.0	V	3
Output saturation voltage 2	V <sub>OU</sub> 2	$I_0 = 0.8 \text{ A}, \text{RF} = 0 \Omega, \text{ Source + Sink}$		1.8	2.6	V	4
MC pin common-mode input voltage range	V <sub>IC</sub>		0		V <sub>CC</sub> – 2	V	5
VC pin input bias current	I <sub>VC</sub>	V <sub>C</sub> = 0 V	-2	-1		μA	6
Control start voltage	V <sub>THVC</sub>		2.3	2.55	2.8	V	7
Closed-loop control gain	GMVC	RF = 0.5 Ω	0.75	0.95	1.15	A/V	8
PCOUT output current 1	I <sub>PCOU</sub>	Source side		-90		μA	9
PCOUT output current 2	I <sub>PCOD</sub>	Sink side		90		μA	10
VCOIN input current	I <sub>VCOIN</sub>	V <sub>COIN</sub> = 5 V		0.1	0.2	μA	11
Minimum VCO frequency	fV <sub>COMIN</sub>	$Cx = 0.022 \ \mu\text{F}$ , With V <sub>COIN</sub> open		400		Hz	12
Maximum VCO frequency	fV <sub>COMAX</sub>	Cx = 0.022 µF, V <sub>COIN</sub> = 5 V		18.5		kHz	13
C1/C2 source current ratio	RSOURCE	IC1SOURCE / IC2SOURCE	-12		+12	%	14
C1/C2 sink current ratio	RSINK	IC1SINK / IC2SINK	-12		+12	%	15
C1 source/sink current ratio	RC1	IC1SOURCE / IC1SINK	-35		+15	%	16
C2 source/sink current ratio	RC2	IC2SOURCE / IC2SINK	-35		+15	%	17
S/S pin high level voltage	V <sub>SSH</sub>		4			V	18
S/S pin low level voltage	V <sub>SSL</sub>				0.7	V	19
S/S pin input current	I <sub>SSI</sub>	$V_{S/S} = 5 V$			200	μA	20
Thermal shutdown circuit operating temperature	TTSD		150	180	210	°C	*
Thermal shutdown circuit hysteresis	∆TTSD			15		°C	*
[FG/PG Amplifier Block]							
Back EMF FG							
Output on voltage	V <sub>OL</sub>				0.4	V	21
Output off voltage	V <sub>OH</sub>		V <sub>REG</sub> – 0.5			V	22
PG amplifier							
Input offset voltage	V <sub>IO</sub>		-8		+8	mV	23
Input bias current	I <sub>BIN</sub> -		- 250			nA	24
Common-mode input voltage range	VICOM		0		V <sub>REG</sub> - 1.5	V	*
Open-loop gain	GVPG	f = 1 kHz		55		dB	25
Output on voltage	V <sub>OL</sub>				0.4	V	26
Output off voltage	V <sub>OH</sub>		V <sub>REG</sub> – 0.5			V	27
Schmitt amplifier hysteresis	V <sub>SHIS</sub>		70	93	115	mV	28

Note \* : These are design target values and are not measured.

**Pin Assignment** 





Block Diagram (Note that the external constants will vary depending on the motor used.)

### **Pin Functions**

Pin No.	Pin	Pin voltage	Equivalent circuit	Function
27 1 2	UOUT VOUT WOUT		Vcc 3.9 10k 20 μ	Drum motor driver outputs
26	RF		$ \begin{array}{cccccccccccccccccccccccccccccccccccc$	The lowest potential of the drum motor driver output transistors. Constant-current control is implemented by detecting this voltage. The current limiter also operates by detecting this voltage.
26	S/S		vcc 75k 75k 50k 50k	Driver start/stop control High: Motor drives operating state Low: Standby state (power saving mode)
7	V <sub>CC</sub>	8 to 13.8 V		Power supply
5	МСОМ		VCC - 27(1)2 V V V	Motor coil center input The coil voltage waveform is detected referenced to this voltage.
8	UIN			
9	VIN			Coil waveform detection comparator inputs Each phase output is connected through an internal 10 $k\Omega$ resistor.
10	WIN			
11	C1		$15 \mu \qquad 15 \mu \qquad 11 \qquad 5 \mu \qquad 12 \qquad 0 \qquad $	Sawtooth waveform generator capacitor connection
12	C2		2S 1k 1/2VREG-VF	This sawtooth waveform is used for soft switching in the coil output waveform.

Continued on next page.

Continued from preceding page.

Pin No.	Pin	Pin voltage	Equivalent circuit	Function
13	сх		VCC VREG 100 µ 300 13 mmm	The value of the capacitor connected between this pin and ground determines the operating frequency range and the minimum operating frequency for the VCO circuit.
14	VCOIN		$1.75V + 1.4 + 50\mu + 50\mu$	VCO circuit voltage input The PCOUT pin voltage is RC filtered and the result is input to this pin.
15	PCOUT		VREG VCC	VCO circuit PLL output
16	GND			Ground for all circuits other than the output transistor
17	VREG		Vcc 17 39k 13k 125V 1.25V	Internal 5 V regulator This pin provides the control system power.

Continued on next page.

Continued from preceding page.

Pin No.	Pin	Pin voltage	Equivalent circuit	Function
18	PGIN+		$VCC \qquad $	PG amplifier positive (+) input This pin is biased internally by 1/2 VREG.
19	PGIN-		19 19 10 10 10 10 10 10 10 10 10 10	PG amplifier negative (–) input
20	PGOUT1		$\begin{array}{c} & & & & \\ & & & & \\ & & & & \\ & & & & $	PG amplifier linear output
21	PGOUT2		VCC $VREG+VF$ $VREG$ $VREG$ $VREG$	PG Schmitt amplifier output
22	BFGO			Motor back EMF detection FG output (synthesized from 3 phases)

Continued on next page.

Continued from preceding page.

Pin No.	Pin	Pin voltage	Equivalent circuit	Function
23	FC		VREG VCC 23 10k \$5k	Frequency characteristics correction Current control system closed loop oscillation can be stopped by inserting a capacitor between this pin and ground.
24	VC	0 to V <sub>CC</sub>	VCC $50 \mu$ $50 \mu$ $27k$ 24 $40k$ $200$ $24k$ $24k$	Speed control This IC implements constant-current control by applying feedback from RF.

Allowable Internal Power Dissipation



- Specifications of any and all SANYO products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.
- SANYO Electric Co., Ltd. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.
- In the event that any or all SANYO products (including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of SANYO Electric Co., Ltd.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the SANYO product that you intend to use.
- Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production. SANYO believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.

This catalog provides information as of December, 1999. Specifications and information herein are subject to change without notice.

This datasheet has been download from:

www.datasheetcatalog.com

Datasheets for electronics components.