

PIC16F882/883/884/886/887

FIGURE 2-6: PIC16F886/PIC16F887 SPECIAL FUNCTION REGISTERS

| File Address | | File Address | | File Address | | File Address | |
|-------------------------------|-----|-------------------------------|-----|-------------------------------|------|-------------------------------|------|
| Indirect addr. ⁽¹⁾ | 00h | Indirect addr. ⁽¹⁾ | 80h | Indirect addr. ⁽¹⁾ | 100h | Indirect addr. ⁽¹⁾ | 180h |
| TMR0 | 01h | OPTION_REG | 81h | TMR0 | 101h | OPTION_REG | 181h |
| PCL | 02h | PCL | 82h | PCL | 102h | PCL | 182h |
| STATUS | 03h | STATUS | 83h | STATUS | 103h | STATUS | 183h |
| FSR | 04h | FSR | 84h | FSR | 104h | FSR | 184h |
| PORTA | 05h | TRISA | 85h | WDTCON | 105h | SRCON | 185h |
| PORTB | 06h | TRISB | 86h | PORTB | 106h | TRISB | 186h |
| PORTC | 07h | TRISC | 87h | CM1CON0 | 107h | BAUDCTL | 187h |
| PORTD ⁽²⁾ | 08h | TRISD ⁽²⁾ | 88h | CM2CON0 | 108h | ANSEL | 188h |
| PORTE | 09h | TRISE | 89h | CM2CON1 | 109h | ANSELH | 189h |
| PCLATH | 0Ah | PCLATH | 8Ah | PCLATH | 10Ah | PCLATH | 18Ah |
| INTCON | 0Bh | INTCON | 8Bh | INTCON | 10Bh | INTCON | 18Bh |
| PIR1 | 0Ch | PIE1 | 8Ch | EEDAT | 10Ch | EECON1 | 18Ch |
| PIR2 | 0Dh | PIE2 | 8Dh | EEADR | 10Dh | EECON2 ⁽¹⁾ | 18Dh |
| TMR1L | 0Eh | PCON | 8Eh | EEDATH | 10Eh | Reserved | 18Eh |
| TMR1H | 0Fh | OSCCON | 8Fh | EEADRH | 10Fh | Reserved | 18Fh |
| T1CON | 10h | OSCTUNE | 90h | | 110h | | 190h |
| TMR2 | 11h | SSPCON2 | 91h | | 111h | | 191h |
| T2CON | 12h | PR2 | 92h | | 112h | | 192h |
| SSPBUF | 13h | SSPADD | 93h | | 113h | | 193h |
| SSPCON | 14h | SSPSTAT | 94h | | 114h | | 194h |
| CCPR1L | 15h | WPUB | 95h | | 115h | | 195h |
| CCPR1H | 16h | IOCB | 96h | General Purpose Registers | 116h | General Purpose Registers | 196h |
| CCP1CON | 17h | VRCON | 97h | | 117h | | 197h |
| RCSTA | 18h | TXSTA | 98h | | 118h | | 198h |
| TXREG | 19h | SPBRG | 99h | 16 Bytes | 119h | 16 Bytes | 199h |
| RCREG | 1Ah | SPBRGH | 9Ah | | 11Ah | | 19Ah |
| CCPR2L | 1Bh | PWM1CON | 9Bh | | 11Bh | | 19Bh |
| CCPR2H | 1Ch | ECCPAS | 9Ch | | 11Ch | | 19Ch |
| CCP2CON | 1Dh | PSTRCON | 9Dh | | 11Dh | | 19Dh |
| ADRESH | 1Eh | ADRESL | 9Eh | | 11Eh | | 19Eh |
| ADCON0 | 1Fh | ADCON1 | 9Fh | | 11Fh | | 19Fh |
| | 20h | | A0h | | 120h | | 1A0h |
| General Purpose Registers | 3Fh | General Purpose Registers | | General Purpose Registers | | General Purpose Registers | |
| | 40h | | | | | | |
| | | 80 Bytes | | 80 Bytes | | 80 Bytes | |
| 96 Bytes | 6Fh | | EFh | | 16Fh | | 1EFh |
| | 70h | accesses | F0h | accesses | 170h | accesses | 1F0h |
| | 7Fh | 70h-7Fh | FFh | 70h-7Fh | 17Fh | 70h-7Fh | 1FFh |
| Bank 0 | | Bank 1 | | Bank 2 | | Bank 3 | |

Unimplemented data memory locations, read as '0'.
Note 1: Not a physical register.
2: PIC16F887 only.