



Figure 2. Functional Block Diagram.

Pin Functional Description

DRAIN (D) Pin:

Power MOSFET drain connection. Provides internal operating current for both start-up and steady-state operation.

BYPASS (BP) Pin:

Connection point for a 0.1 μF external bypass capacitor for the internally generated 5.8 V supply.

ENABLE/UNDER-VOLTAGE (EN/UV) Pin:

This pin has dual functions: enable input and line under-voltage sense. During normal operation, switching of the power MOSFET is controlled by this pin. MOSFET switching is terminated when a current greater than 240 μA is drawn from this pin under most loads. At near maximum loading, even when more than 240 μA is drawn from this pin the MOSFET still switches, but at a predetermined lower current limit. This pin also senses line under-voltage conditions through an external resistor connected to the DC line voltage. If there is no external resistor connected to this pin, *TinySwitch-II* detects its absence and disables the line under-voltage function.

P Package (DIP-8B) G Package (SMD-8B)



Figure 3. Pin Configuration.

SOURCE (S) Pin:

Control circuit common, internally connected to output MOSFET source.

SOURCE (HV RTN) Pin:

Output MOSFET source connection for high voltage return.

